

Perugia 10M MP Build(A03)

2009.05.05

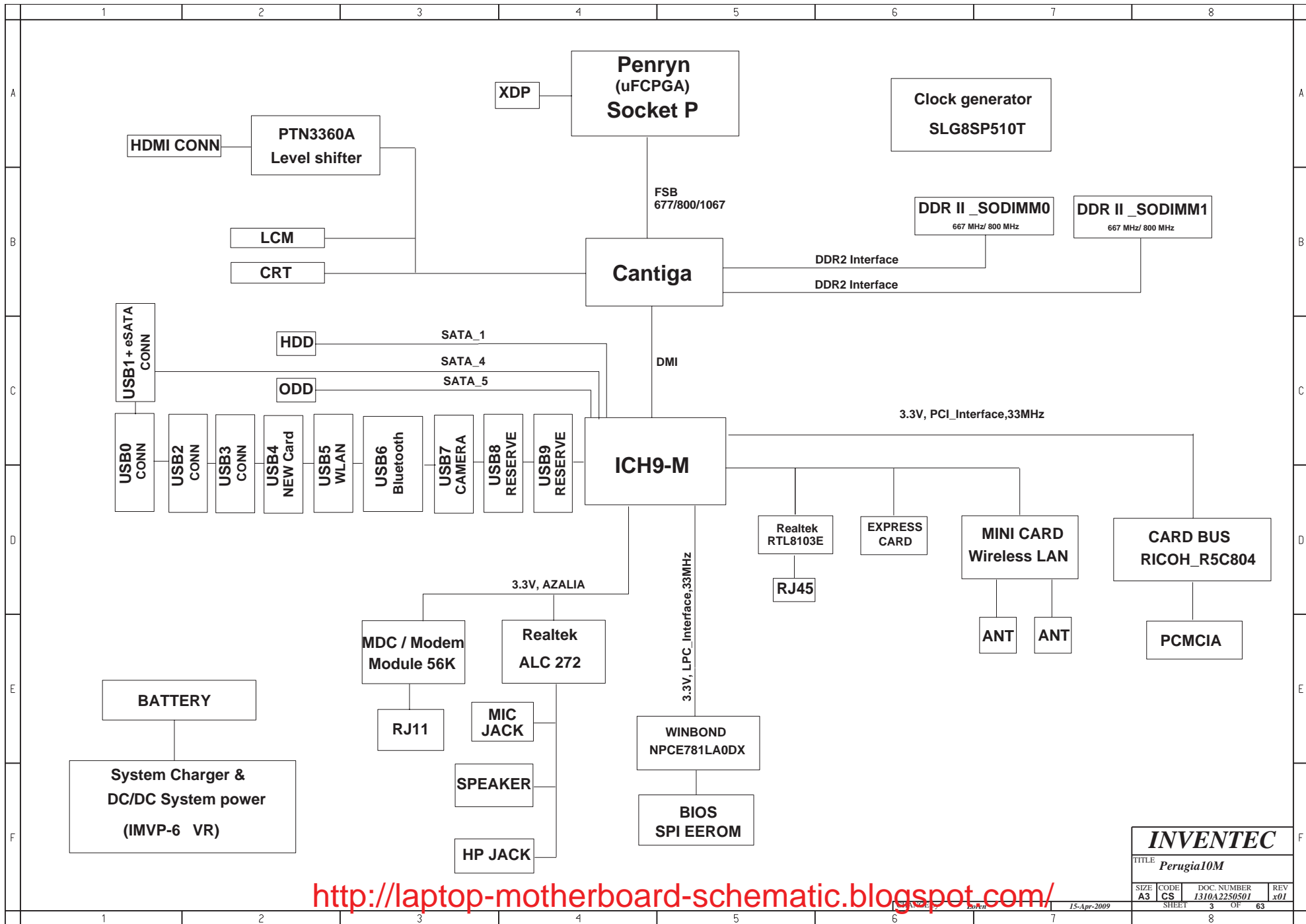
DATE	CHANGE NO.	REV

DRAWER	EE	DATE	POWER	DATE	INVENTEC
DESIGN					
CHECK					
RESPONSIBLE					
TITLE					Perugia10M
SIZE	A3	CODE	CS	DOC. NUMBER	1310A2250501
VER.				REV	x01
SHEET					1 OF 63

<http://laptop-motherboard-schematic.blogspot.com/>

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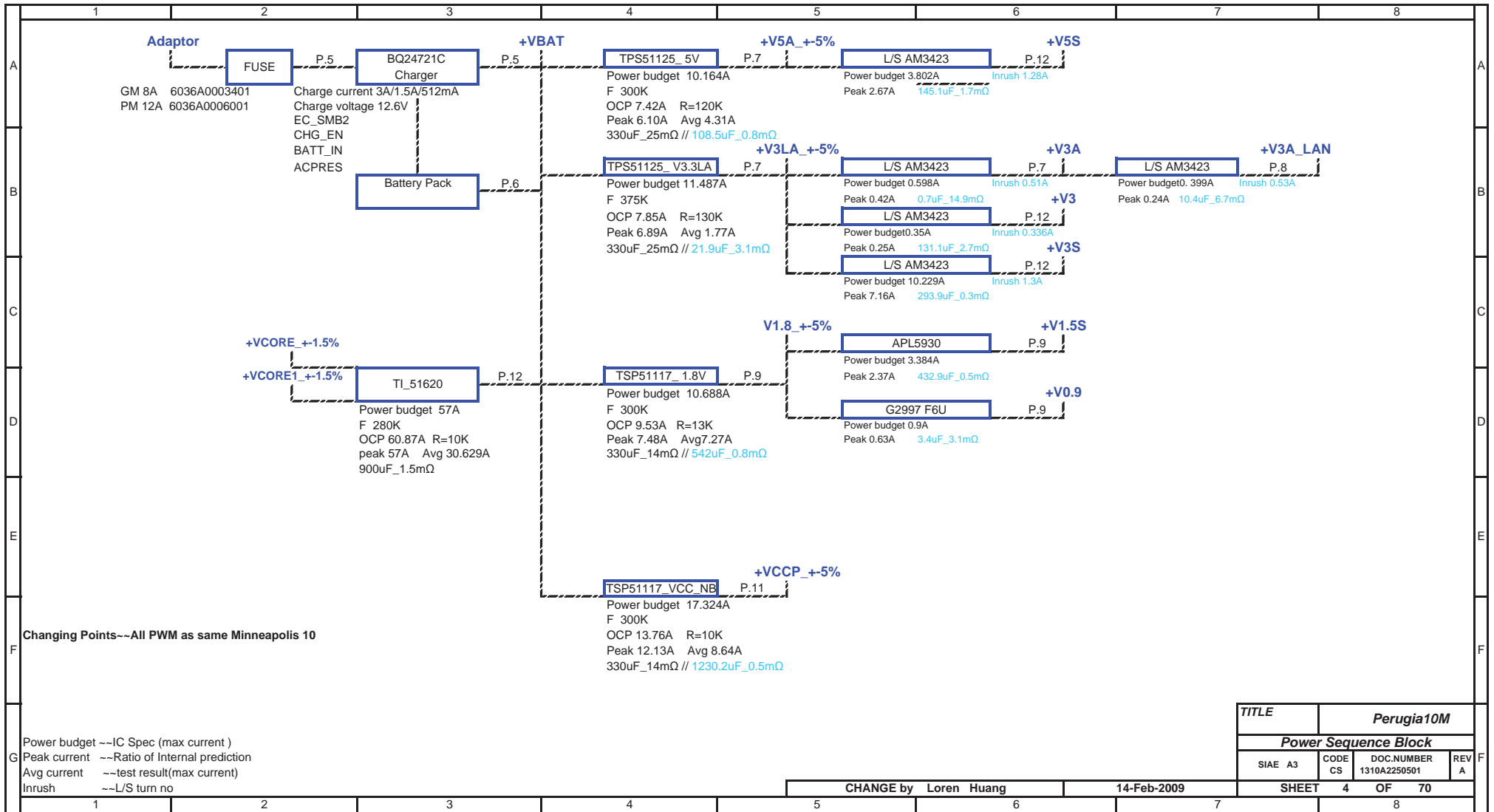
PAGE	PAGE	PAGE
1.COVER PAGE	31.ICH9-1	54.WLAN /Debug
2.INDEX	32.ICH9-2	55.MDC 1.5 CONN
3.BLOCK DIAGRAM	33.ICH9-3	56.LED(M/B) & HOTKEY/B CONN
4.POWER SEQUENCE BLOCK	34.ICH9-4	57.KILL SWITCH& HALL SENSOR
5-12.SYSTEM POWER	35.ICH9-5	58.Screw
13.CLOCK GENERATOR	36.SATA HDD CONN	59.EMI
14.PENRYN-1	37.ODD CONN	60.3D sensor
15.PENRYN-2	38.USB CONN	61. Hotkey board
16.PENRYN-3	39.ESATA CONN	62. Touch pad board
17.PENRYN-4	40.Camera	63. power board
18.FAN & THERMAL CONTROLLER	41.BLUE TOOTH CONN	
19.CANTIGA-1	42.KBC	
20.CANTIGA-2	43.K/B & TP/B CONN	
21.CANTIGA-3	44.Audio Codec	
22.CANTIGA-4	45.Audio AMP/MIC/Speaker	
23.CANTIGA-5	46.LAN CONTROLLER	
24.CANTIGA-6	47.RJ45 & TRANSFORMER	
25.CANTIGA-7	48.HDMI & CEC	
26.DDR2 DIMM0	49.HDMI CONN	
27.DDR2 DIMM1	50.USB Card Reader	
28.DDR DAMPING	51.Card Bus-1	
29.CRT CONN	52.Card Bus-2	
30.LCM CONN	53.Express Card	

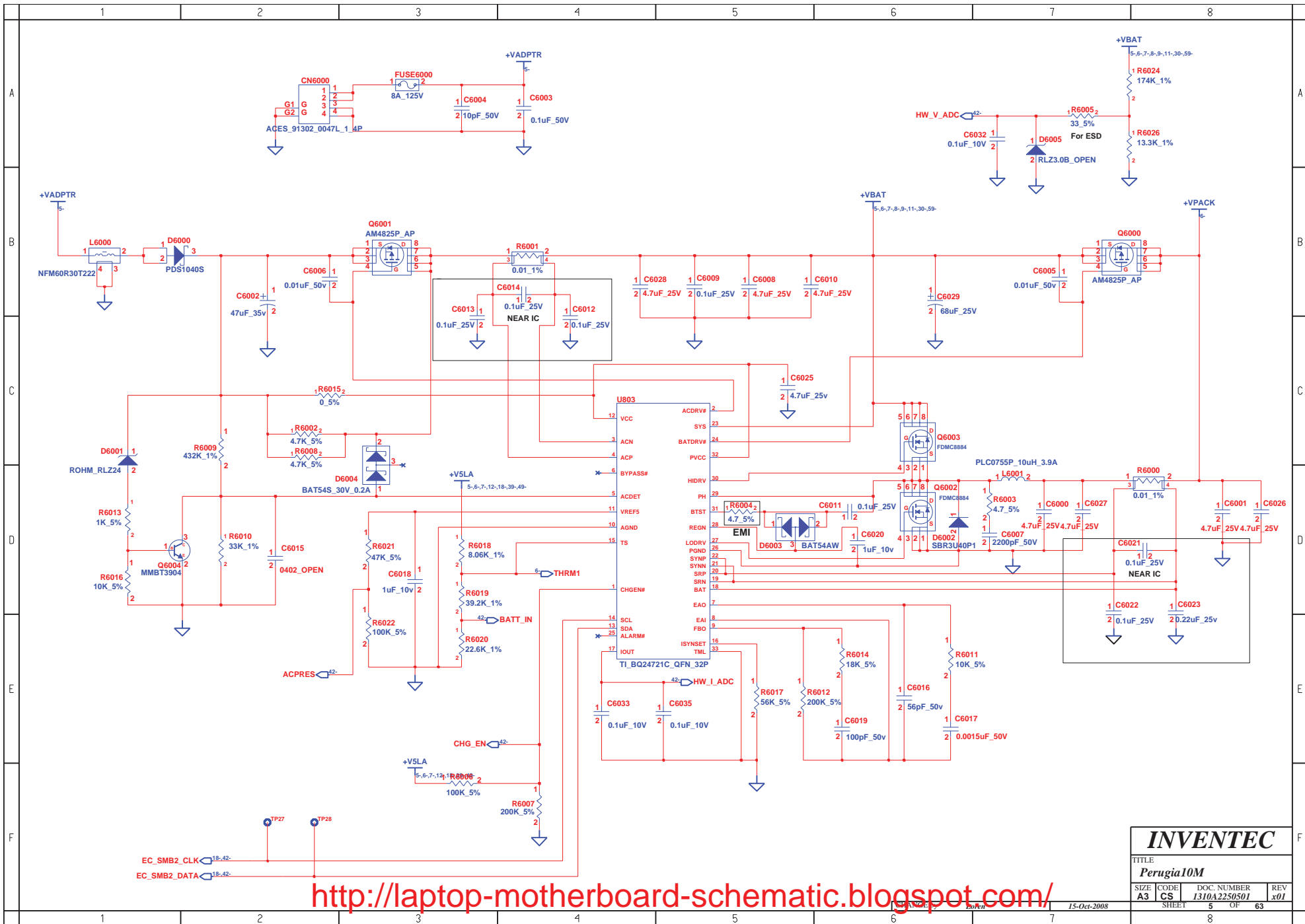


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INVENTEC			
TITLE <i>Perugia10M</i>			
SIZE A3	CODE CS	DOC. NUMBER 1310A2250501	REV x01
SHEET 3		OF 63	

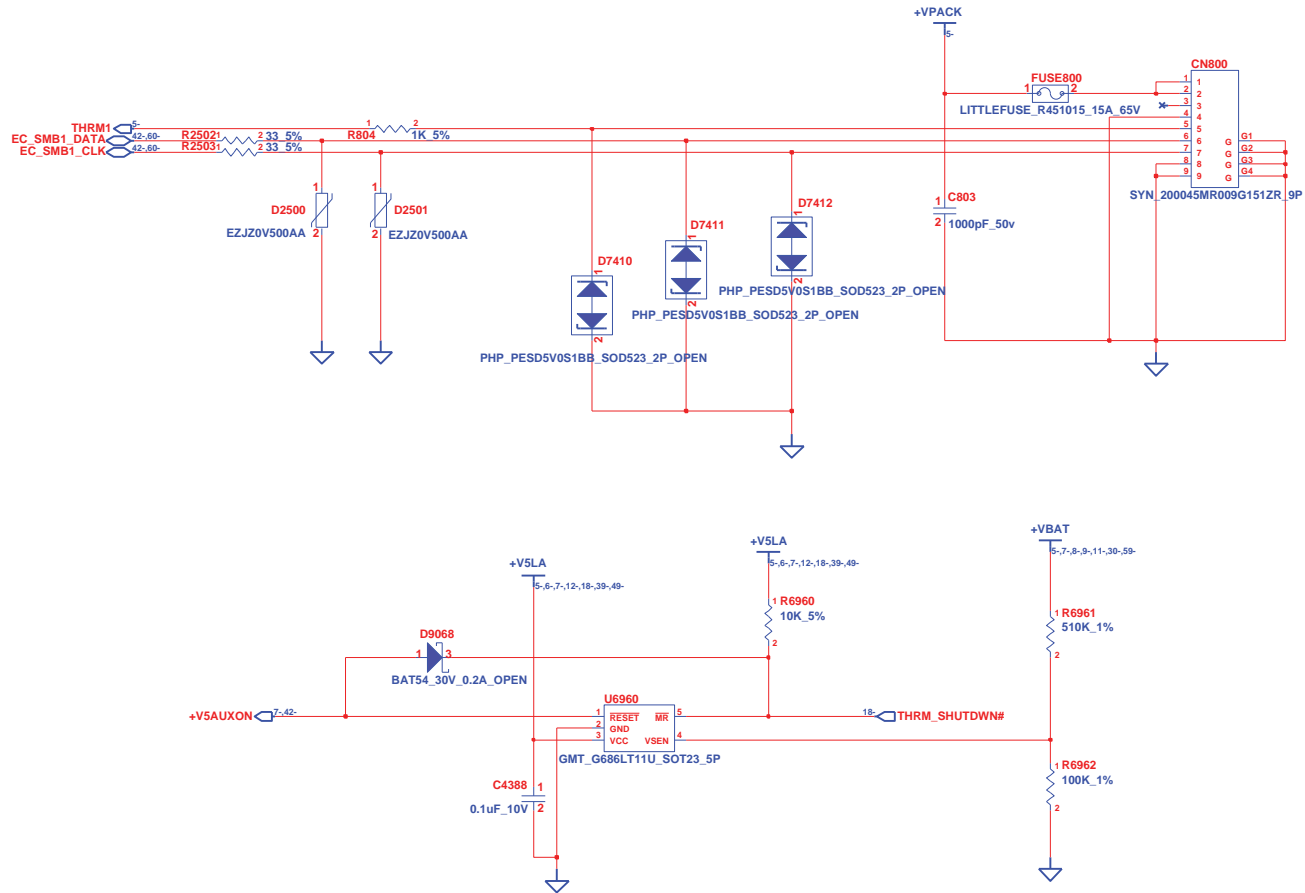
15-Apr-2009





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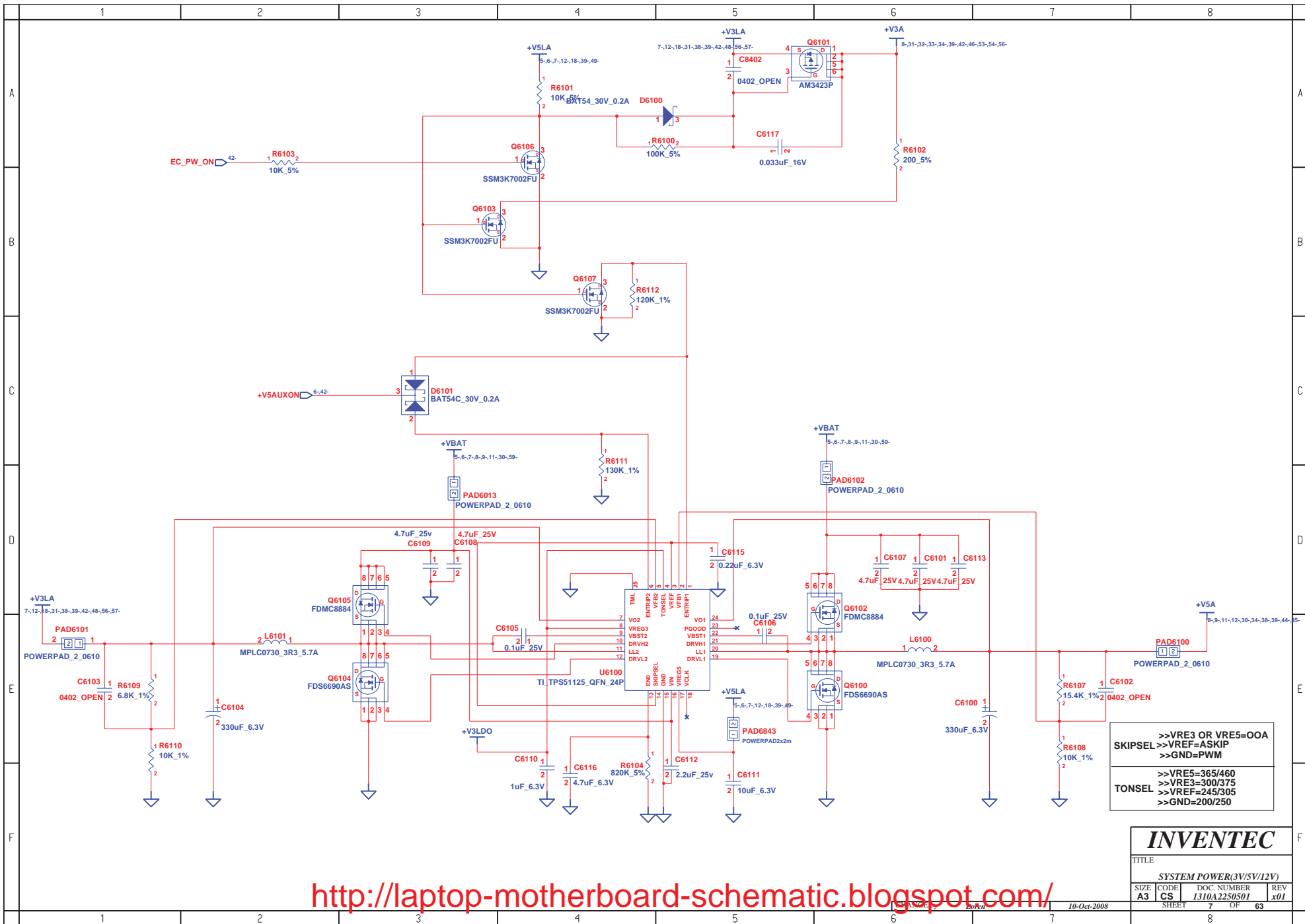
INVENTEC			
TITLE Perugia10M			
SIZE A3	CODE CS	DOC. NUMBER 1310A2250501	REV x01
15-Oct-2008		SHEET 5 OF 63	



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INVENTEC			
TITLE			
SELECT & BATTERY CONN			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	1310A2250501	x01
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10-Oct-2008



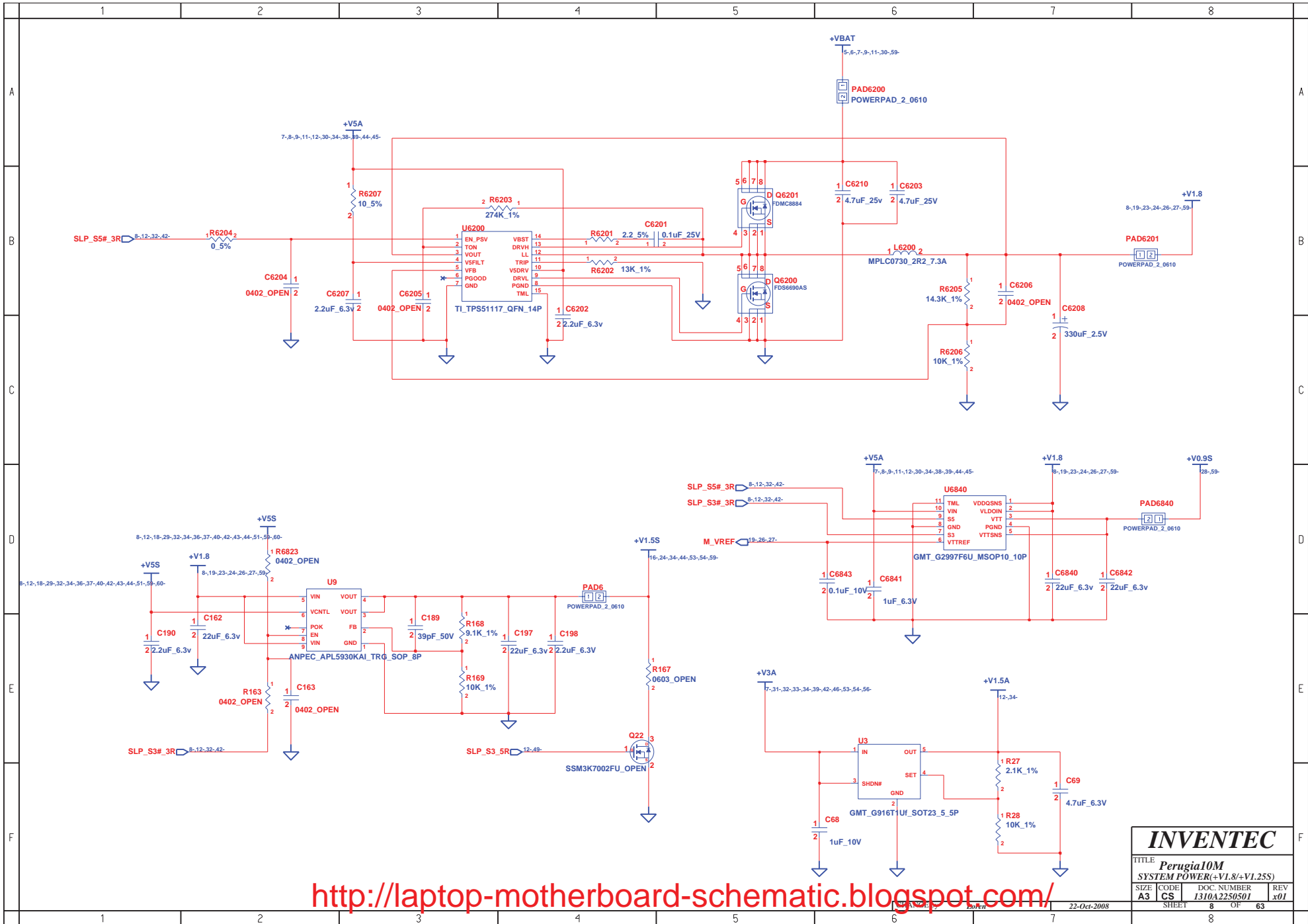
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>>VRE3 OR VRE5=OOA
 SKIPSEL >>VREF=ASKIP
 >>GND=PWM
 >>VRE5=365/460
 >>VRE3=300/375
 TONSEL >>VREF=245/305
 >>GND=200/250

INVENTEC

TITLE			
SYSTEM POWER(3V/5V/12V)			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	1310A2250501	x01
SHEET		7	OF 63

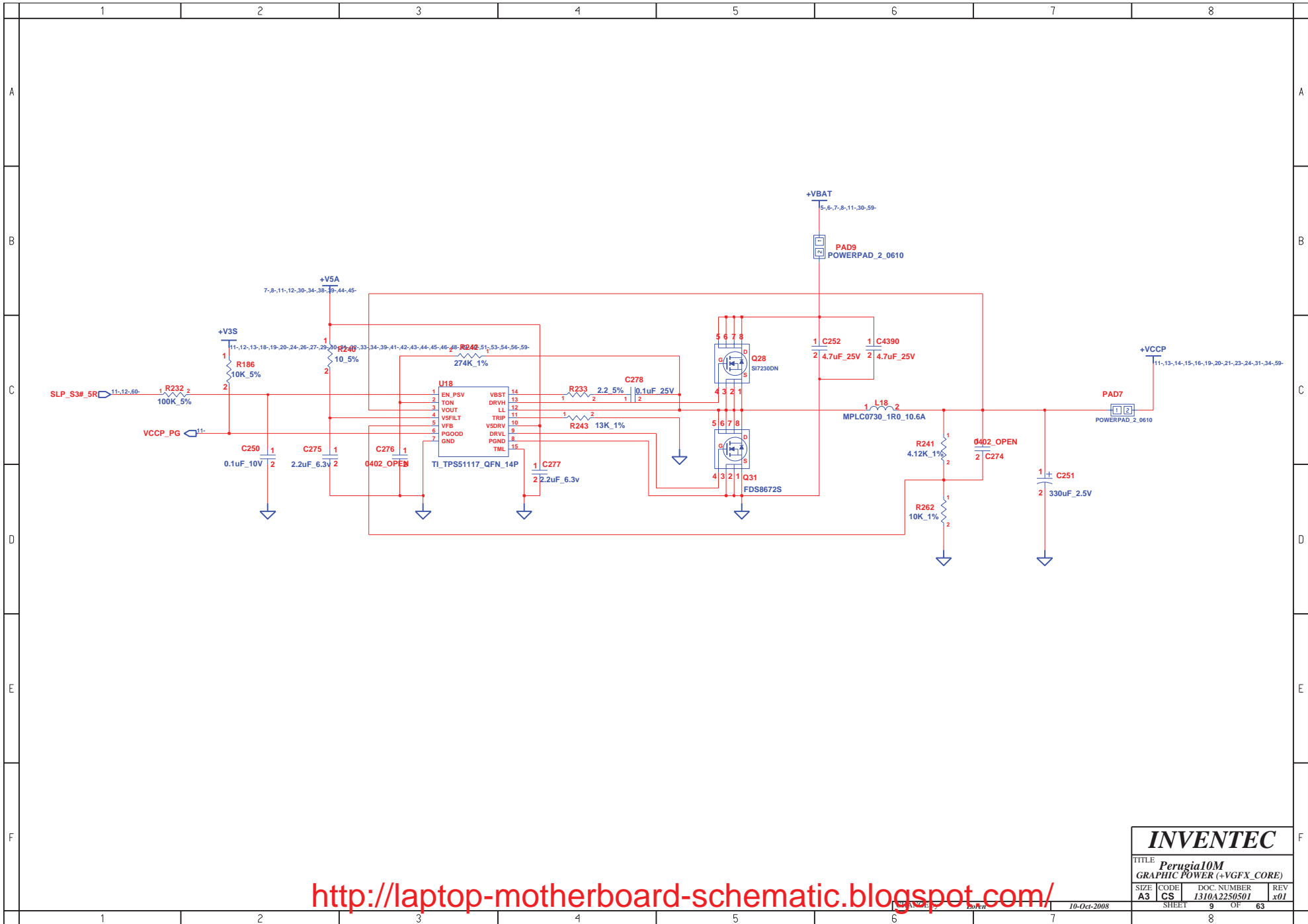
10-Oct-2008



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INVENTEC			
TITLE Perugia10M			
SYSTEM POWER(+V1.8/+V1.25S)			
SIZE A3	CODE CS	DOC. NUMBER 1310A2250501	REV x01
SHEET 8		OF 63	

22-Oct-2008



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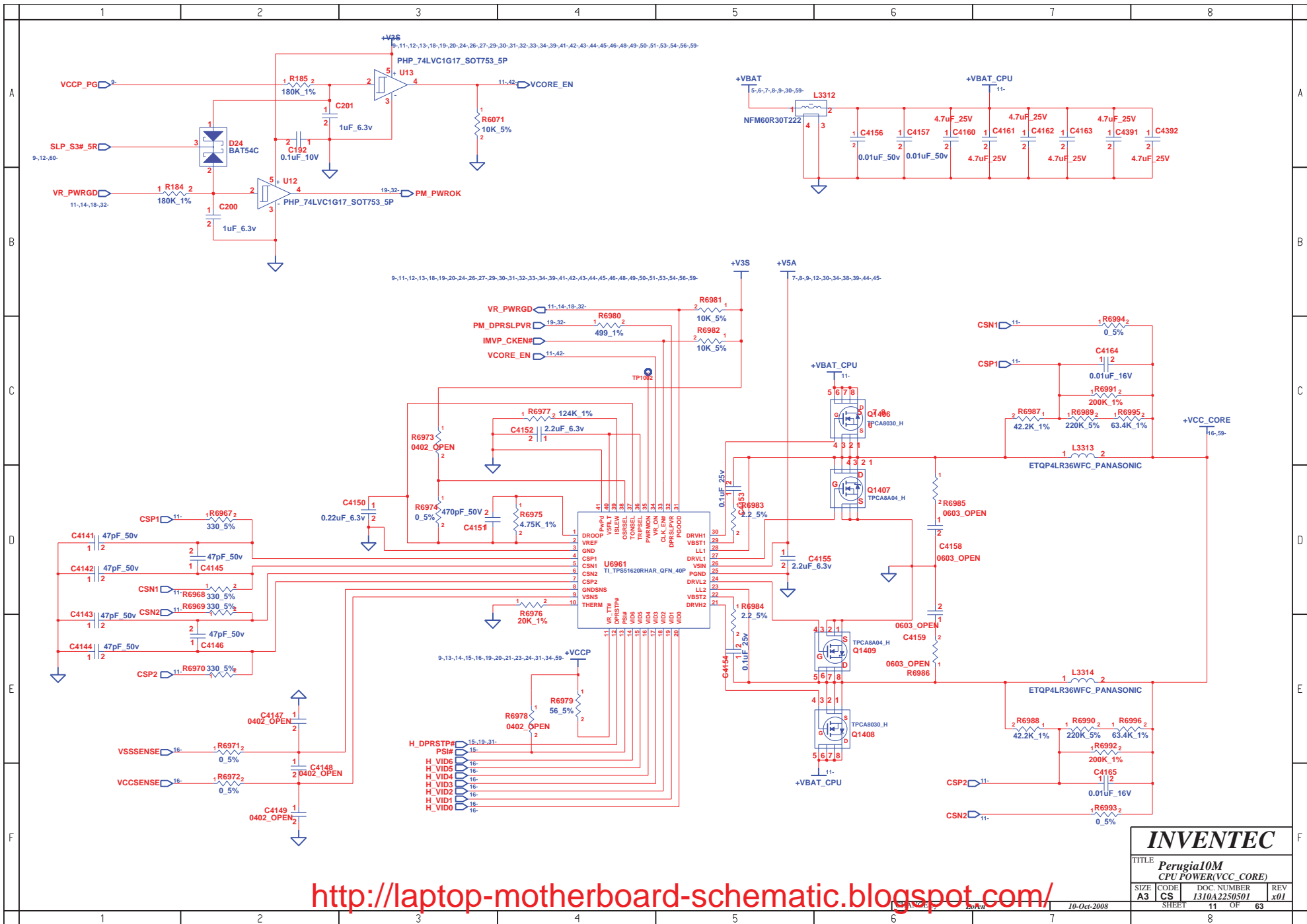
INVENTEC			
TITLE Perugia10M			
GRAPHIC POWER (+VGF_X_CORE)			
SIZE A3	CODE CS	DOC. NUMBER 1310A2250501	REV x01
DATE 10-Oct-2008		SHEET 9 OF 63	

BLANK

INVENTEC			
TITLE Perugia10M			
SYSTEM POWER(+VCCP/A-V1.5S)			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	1310A2250501	x01
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	1	2	3	4	5	6	7	8	
A									A
B									B
C									C
D									D
E									E
F									F
	1	2	3	4	5	6	7	8	

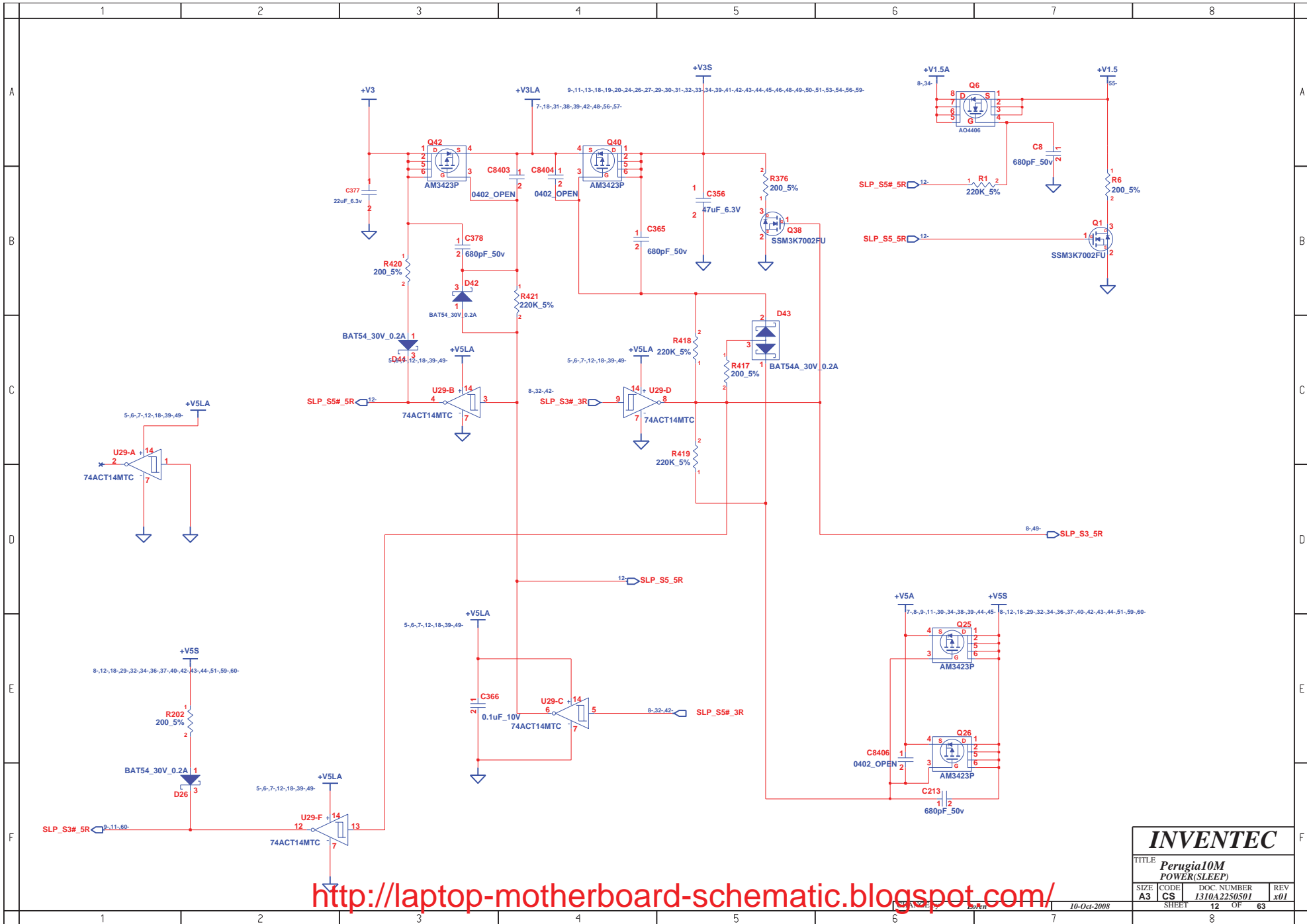


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INVENTEC

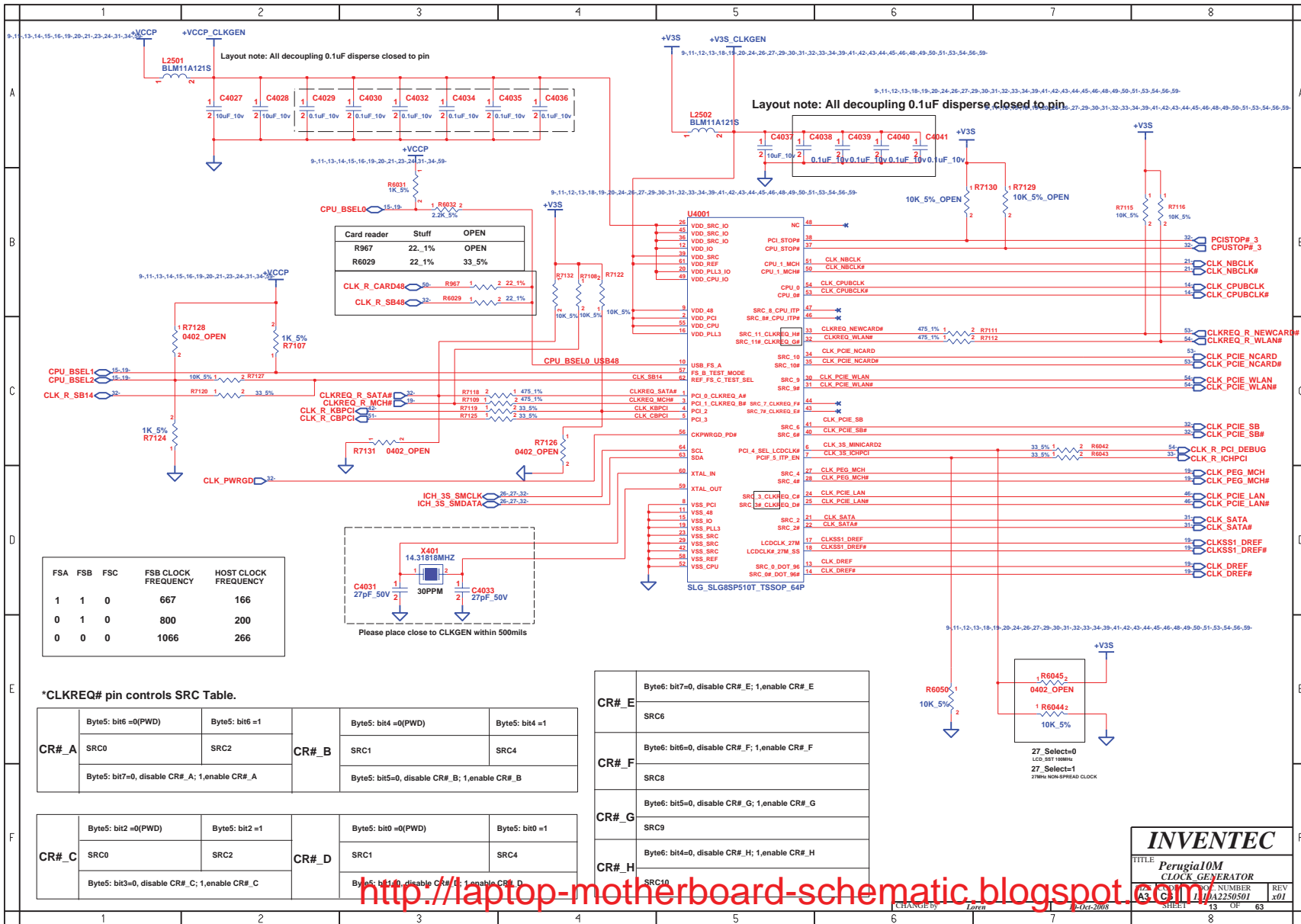
TITLE
Perugia10M
CPU POWER(VCC_CORE)

SIZE A3	CODE CS	DOC NUMBER 1310A2250501	REV x01
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INVENTEC			
TITLE Perugia10M POWER(SLEEP)			
SIZE A3	CODE CS	DOC. NUMBER 1310A2250501	REV x01
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FSA	FSB	FSC	FSB CLOCK FREQUENCY	HOST CLOCK FREQUENCY
1	1	0	667	166
0	1	0	800	200
0	0	0	1066	266

*CLKREQ# pin controls SRC Table.

CR#_A	Byte5: bit6 =0(PWD)		CR#_B	Byte5: bit4 =0(PWD)	
	SRC0	SRC2		SRC1	SRC4
Byte5: bit7=0, disable CR#_A; 1,enable CR#_A		Byte5: bit5=0, disable CR#_B; 1,enable CR#_B			

CR#_C	Byte5: bit2 =0(PWD)		CR#_D	Byte5: bit0 =0(PWD)	
	SRC0	SRC2		SRC1	SRC4
Byte5: bit3=0, disable CR#_C; 1,enable CR#_C		Byte5: bit1=0, disable CR#_D; 1,enable CR#_D			

CR#_E	Byte6: bit7=0, disable CR#_E; 1,enable CR#_E
CR#_F	Byte6: bit6=0, disable CR#_F; 1,enable CR#_F
CR#_G	Byte6: bit5=0, disable CR#_G; 1,enable CR#_G
CR#_H	Byte6: bit4=0, disable CR#_H; 1,enable CR#_H
CR#_I	Byte6: bit3=0, disable CR#_I; 1,enable CR#_I
CR#_J	Byte6: bit2=0, disable CR#_J; 1,enable CR#_J
CR#_K	Byte6: bit1=0, disable CR#_K; 1,enable CR#_K
CR#_L	Byte6: bit0=0, disable CR#_L; 1,enable CR#_L

INVENTEC

TITLE: Perugia10M
CLOCK GENERATOR

REV: 1.0

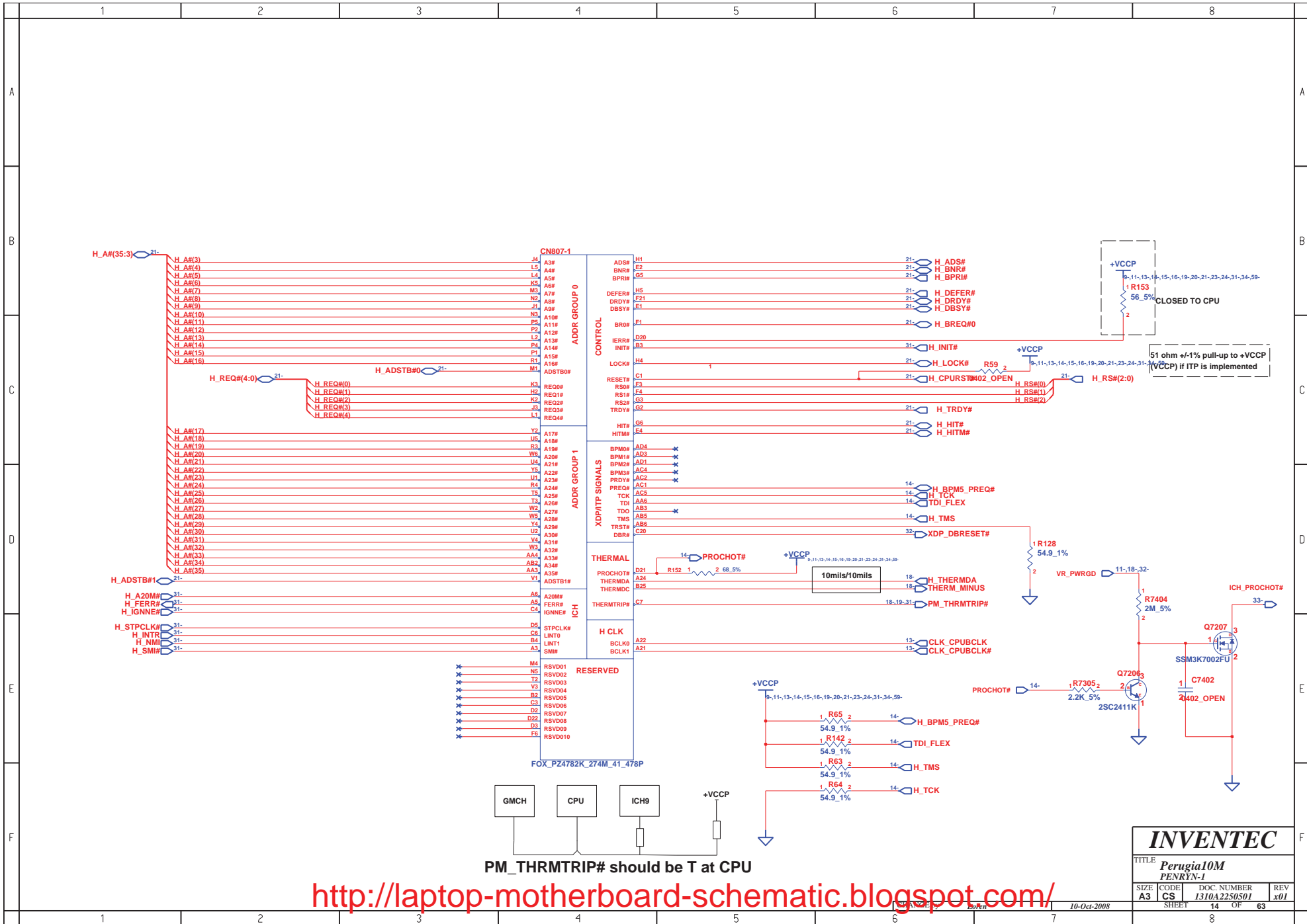
DATE: 11/01/2008

REV: x01

CHANGE BY: Loren

DATE: 10-Dec-2008

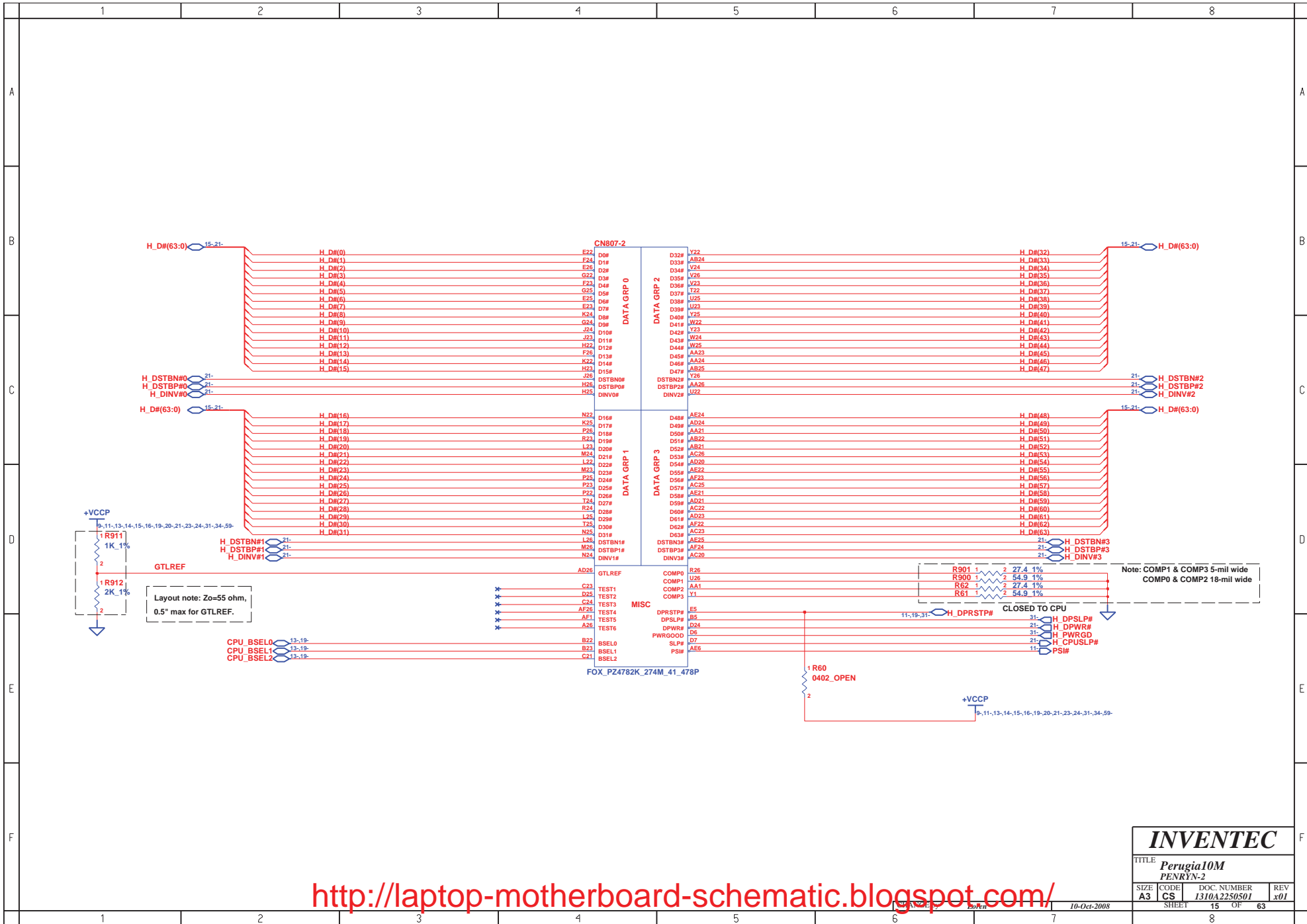
SHEET: 13 OF 63



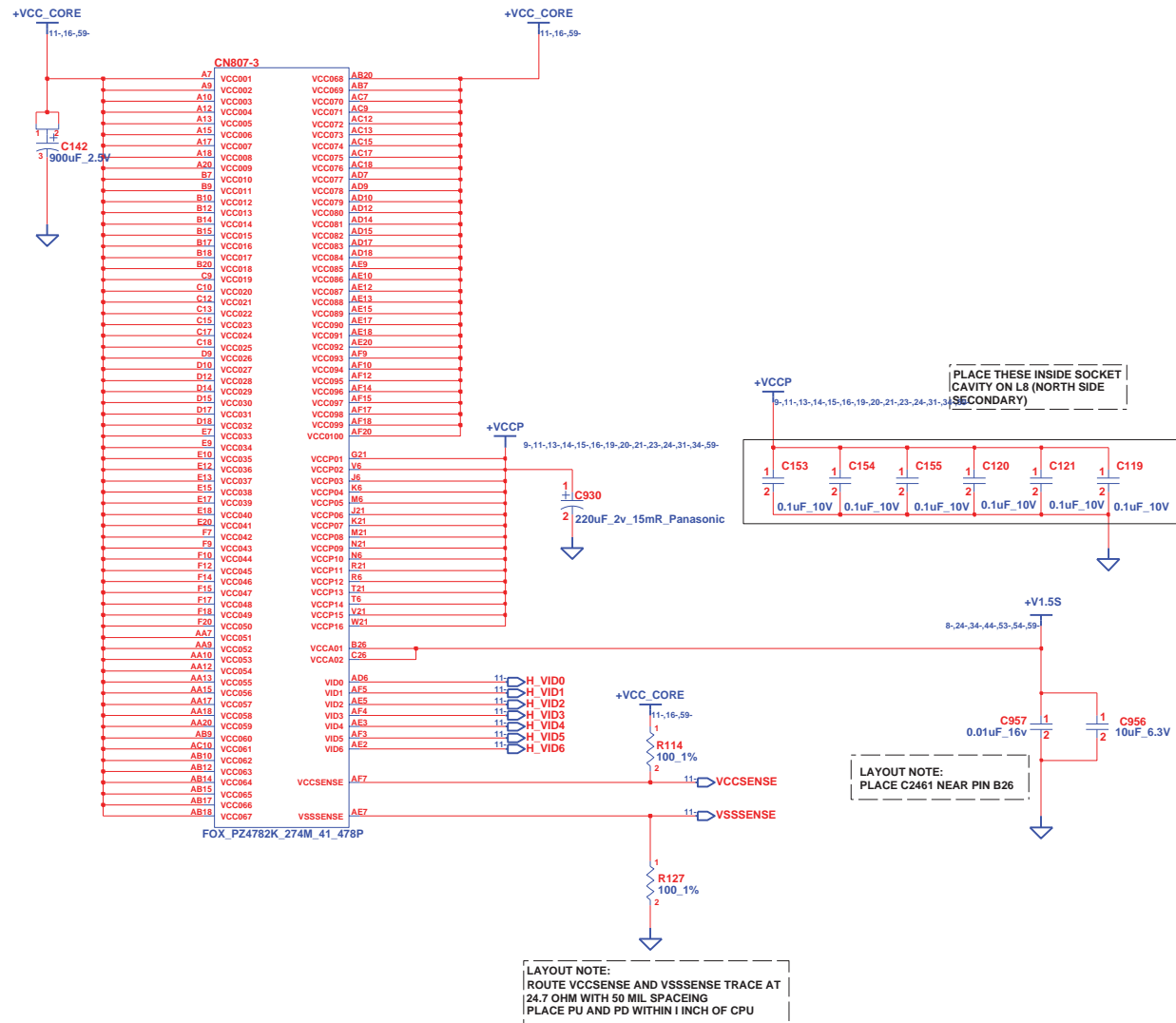
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INVENTEC			
TITLE Perugia10M PENRYN-1			
SIZE A3	CODE CS	DOC. NUMBER 1310A2250501	REV x01
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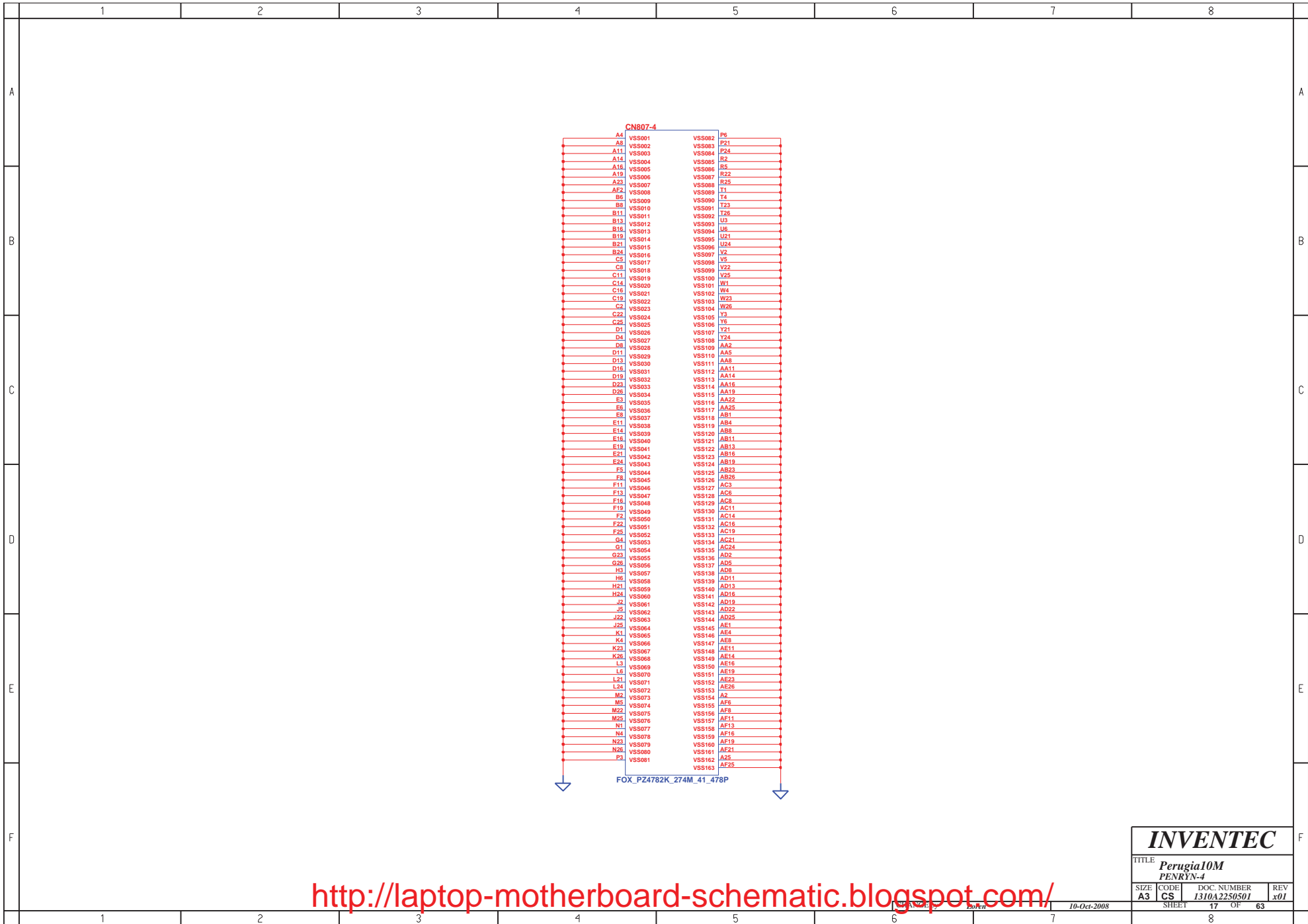
PLACE THESE INSIDE SOCKET CAVITY ON L8 (NORTH SIDE SECONDARY)

LAYOUT NOTE: PLACE C2461 NEAR PIN B26

LAYOUT NOTE: ROUTE VCCSENSE AND VSSSENSE TRACE AT 24.7 OHM WITH 50 MIL SPACING PLACE PU AND PD WITHIN 1 INCH OF CPU

INVENTEC			
TITLE Perugia10M PENRYN-3			
SIZE A3	CODE CS	DOC. NUMBER 1310A2250501	REV x01
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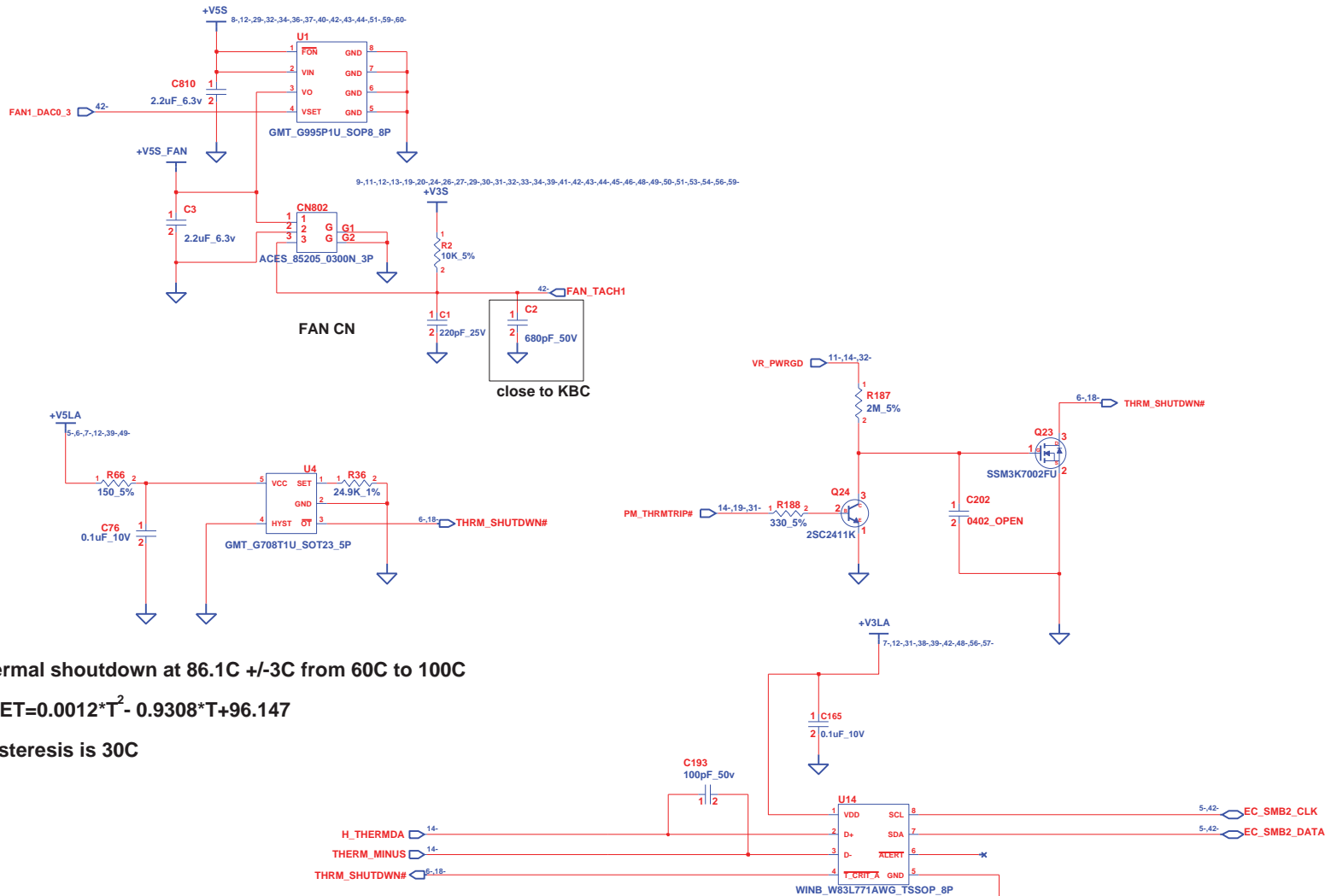


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INVENTEC

TITLE
Perugia10M
PENRYN-4

SIZE A3	CODE CS	DOC. NUMBER 1310A2250501	REV x01
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Thermal shutdown at 86.1C +/-3C from 60C to 100C

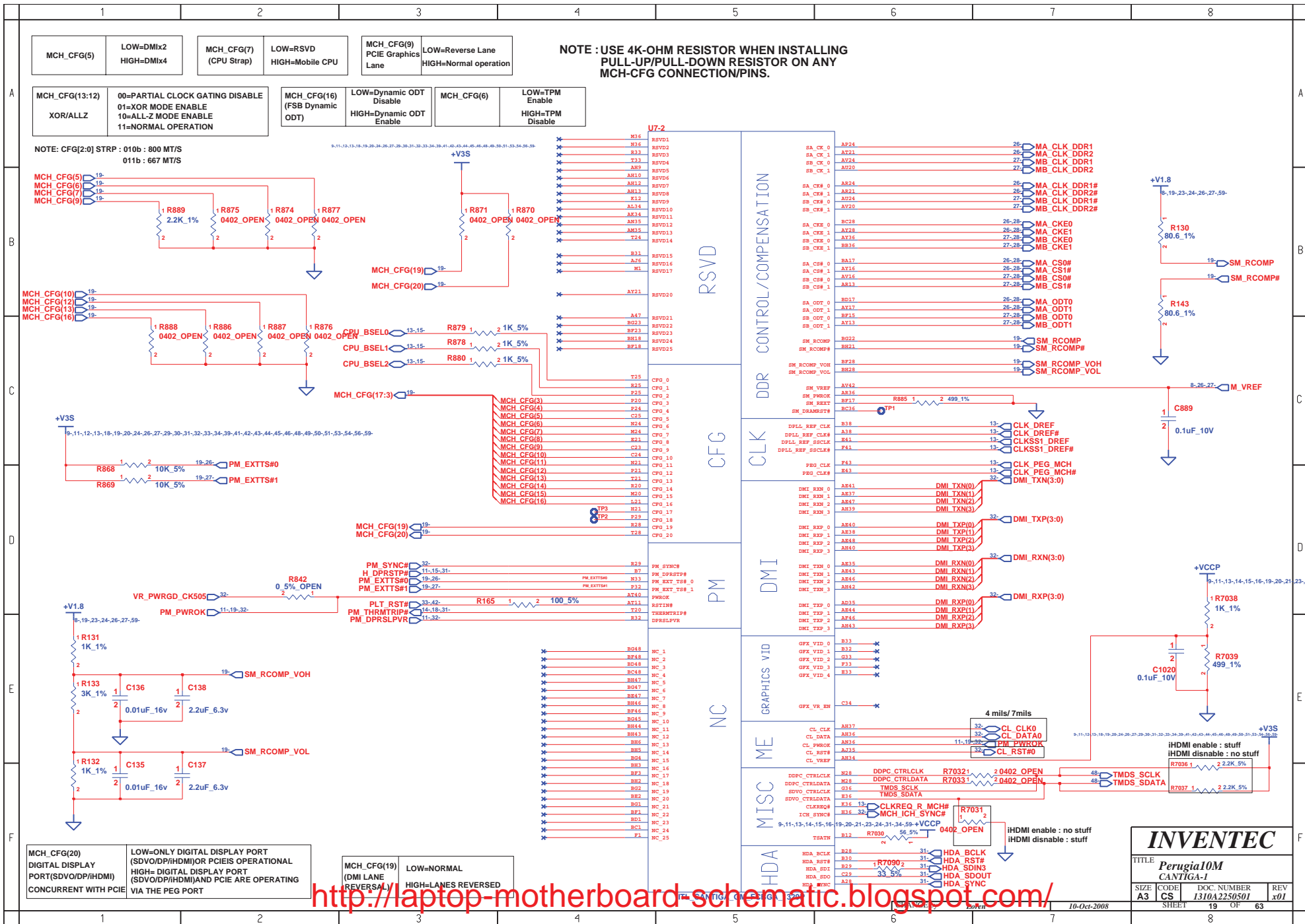
$$RSET=0.0012*T^2 - 0.9308*T+96.147$$

Hysteresis is 30C

Thermal Sensor For CPU
LAYOUT NOTES: PUT THE THERMAL SENSOR CLOSE TO CPU

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INVENTEC			
TITLE Perugia10M THERMAL&FAN CONTROLLER			
SIZE A3	CODE CS	DOC NUMBER 1310A2250501	REV x01
10-Oct-2008		SHEET 18 OF 63	



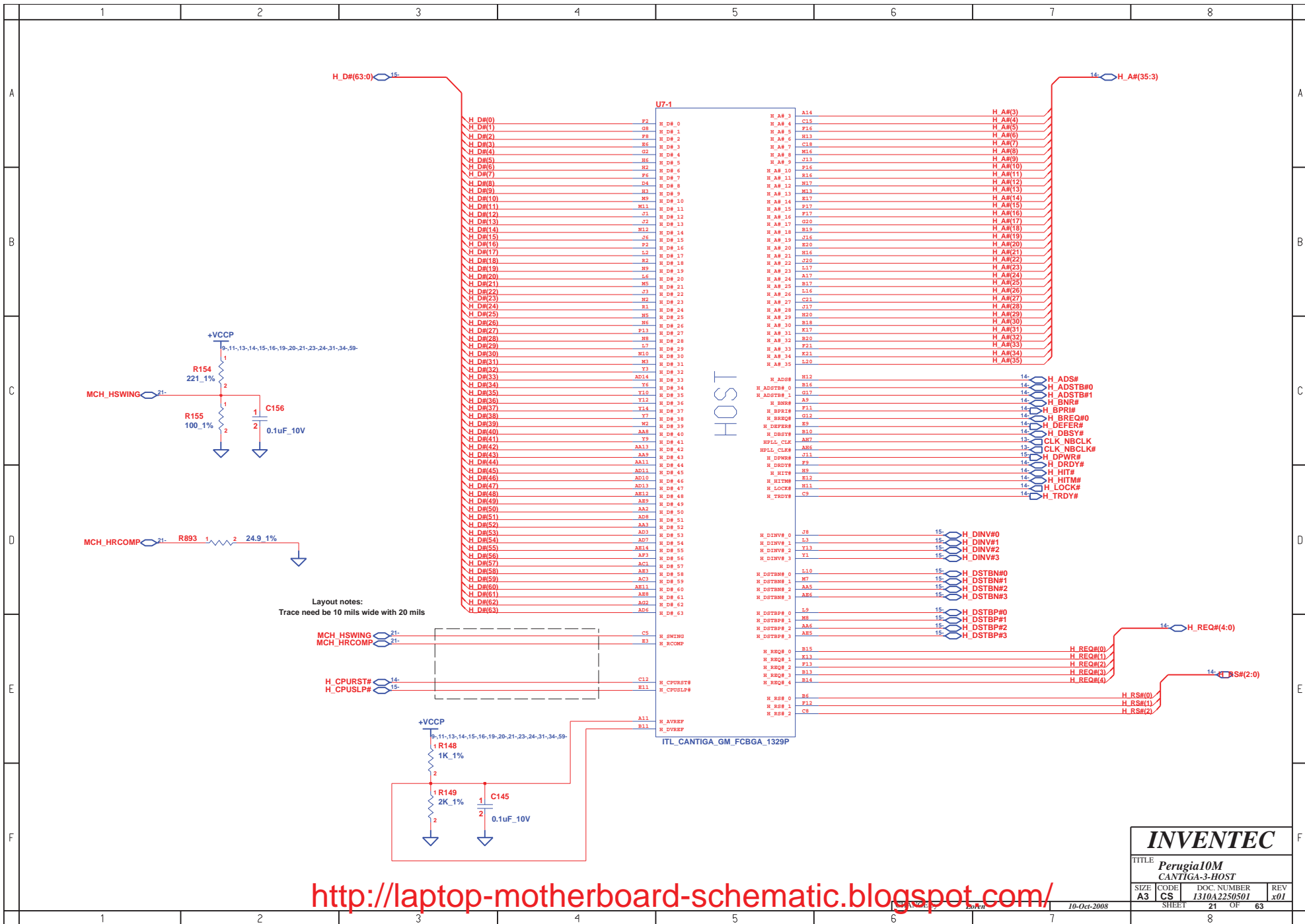
<http://laptop-motherboard-schematic.blogspot.com/>

INVENTEC

TITLE: **Perugia10M Cantiga-1**

SIZE	CODE	DOC. NUMBER	REV
A3	CS	1310A2250501	x01

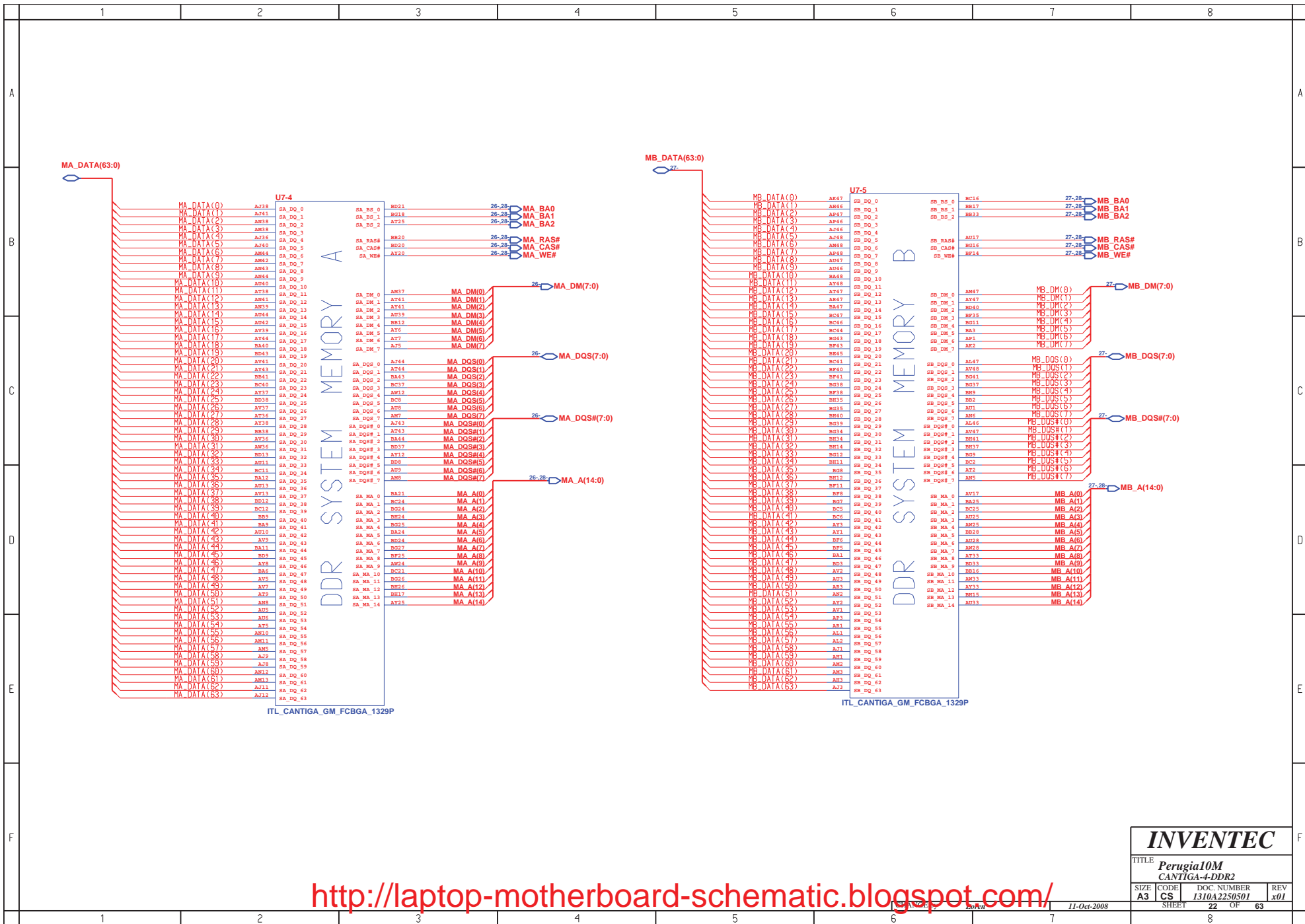
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Layout notes:
Trace need be 10 mils wide with 20 mils

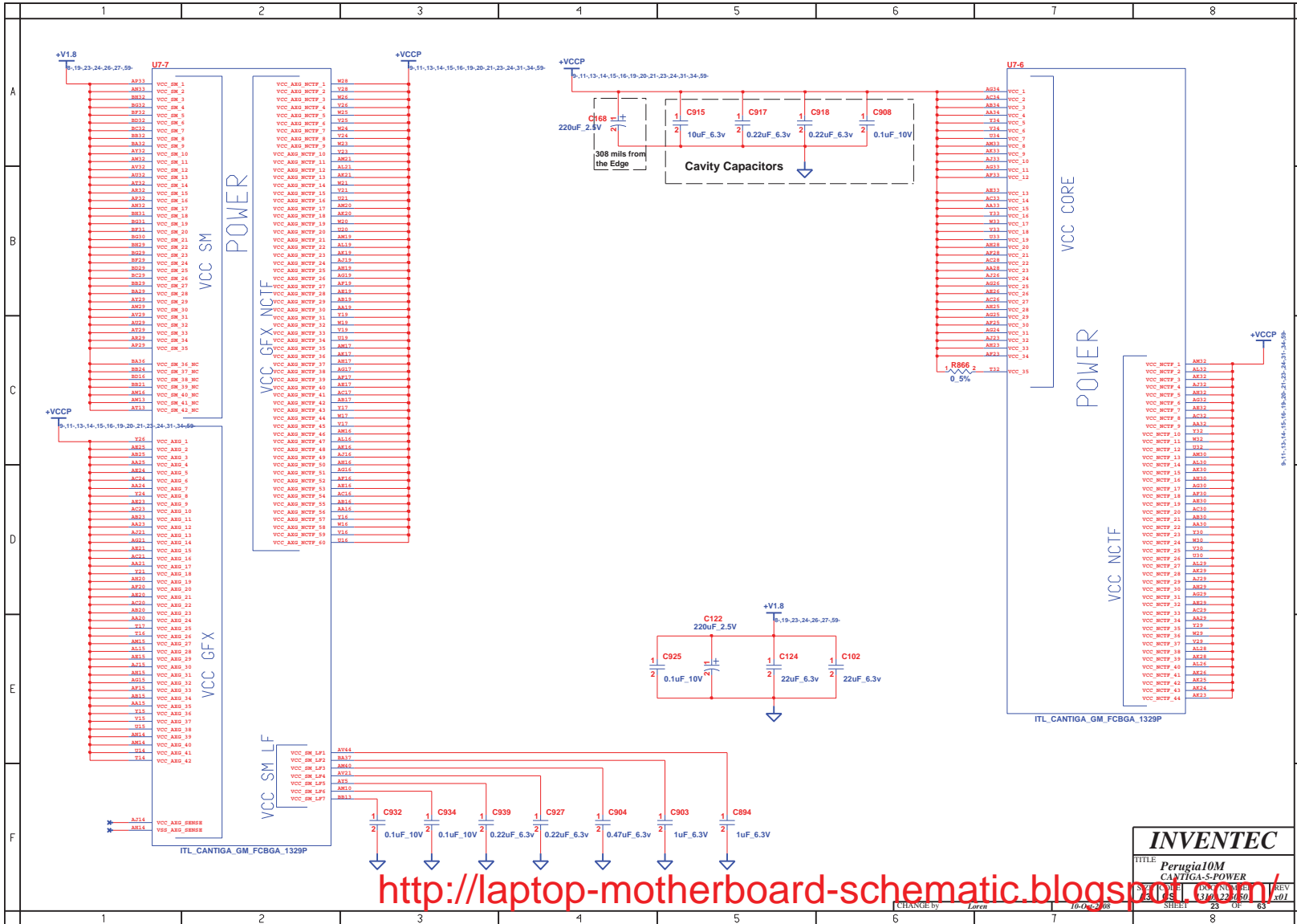
<http://laptop-motherboard-schematic.blogspot.com/>

INVENTEC			
TITLE Perugia10M CANTIGA-3-HOST			
SIZE A3	CODE CS	DOC. NUMBER 1310A2250501	REV x01
SHEET 21		OF 63	



INVENTEC			
TITLE Perugia10M CANTIGA-4-DDR2			
SIZE A3	CODE CS	DOC. NUMBER 1310A2250501	REV x01
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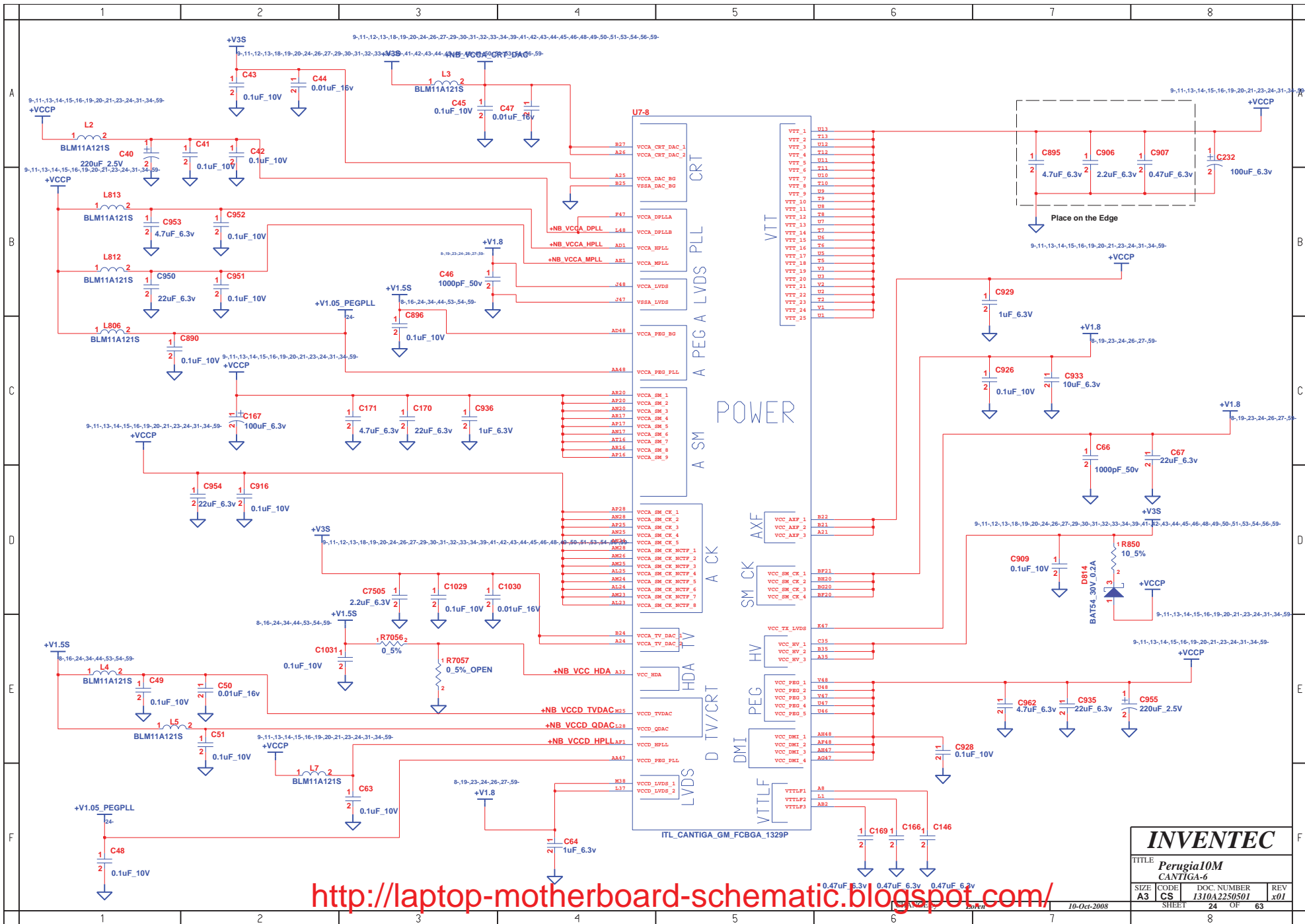
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INVENTEC
 TITLE: Perugia10M CANTIGA-5-POWER
 REV: 1.0 (10/22/05)
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CHANGE by Loren 10/22/05



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INVENTEC			
TITLE Perugia10M CANTIGA-6			
SIZE A3	CODE CS	DOC NUMBER 1310A2250501	REV x01
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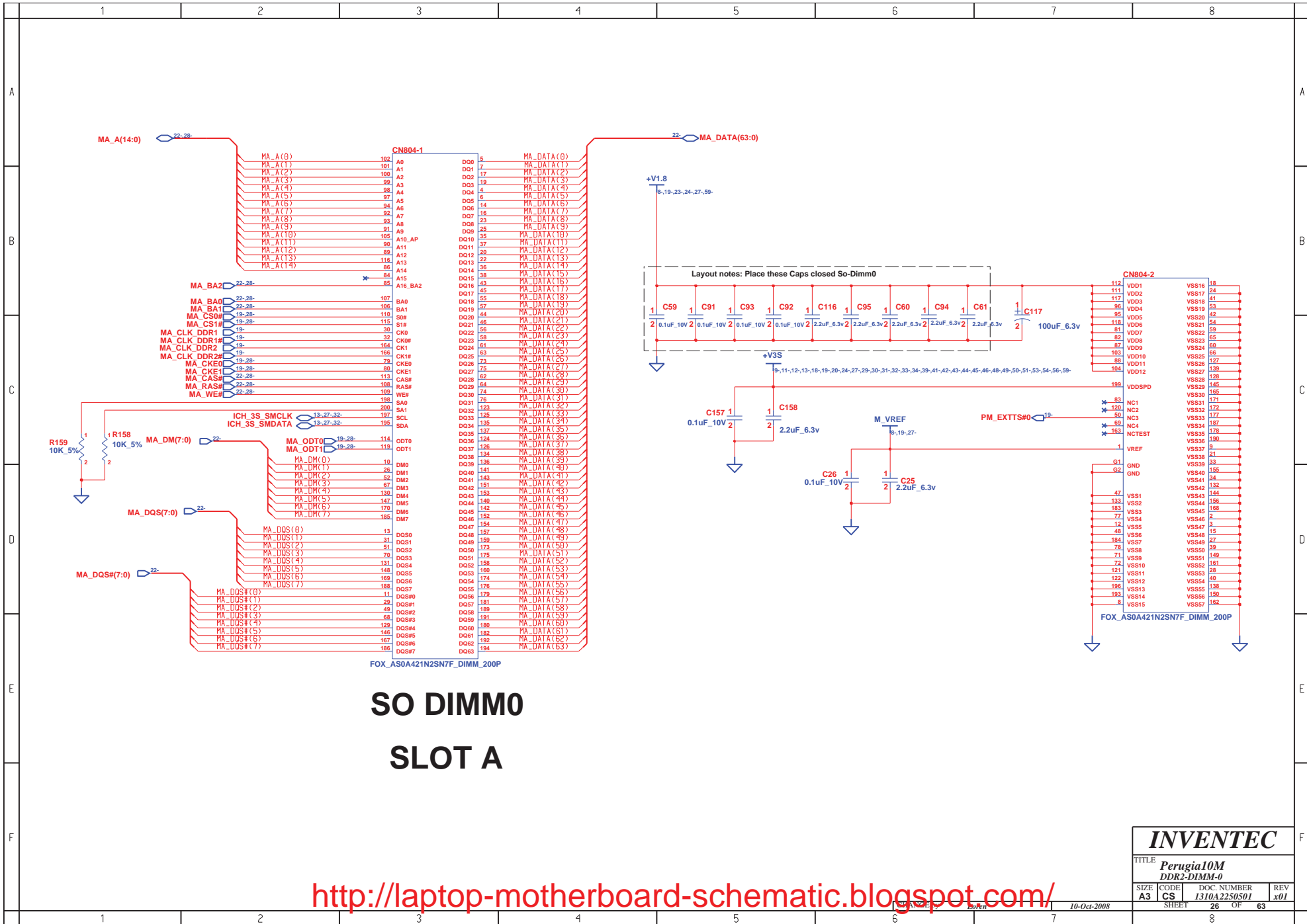
10-Oct-2008



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INVENTEC			
TITLE Perugia10M CANTIGA-7			
SIZE A3	CODE CS	DOC. NUMBER 1310A2250501	REV x01
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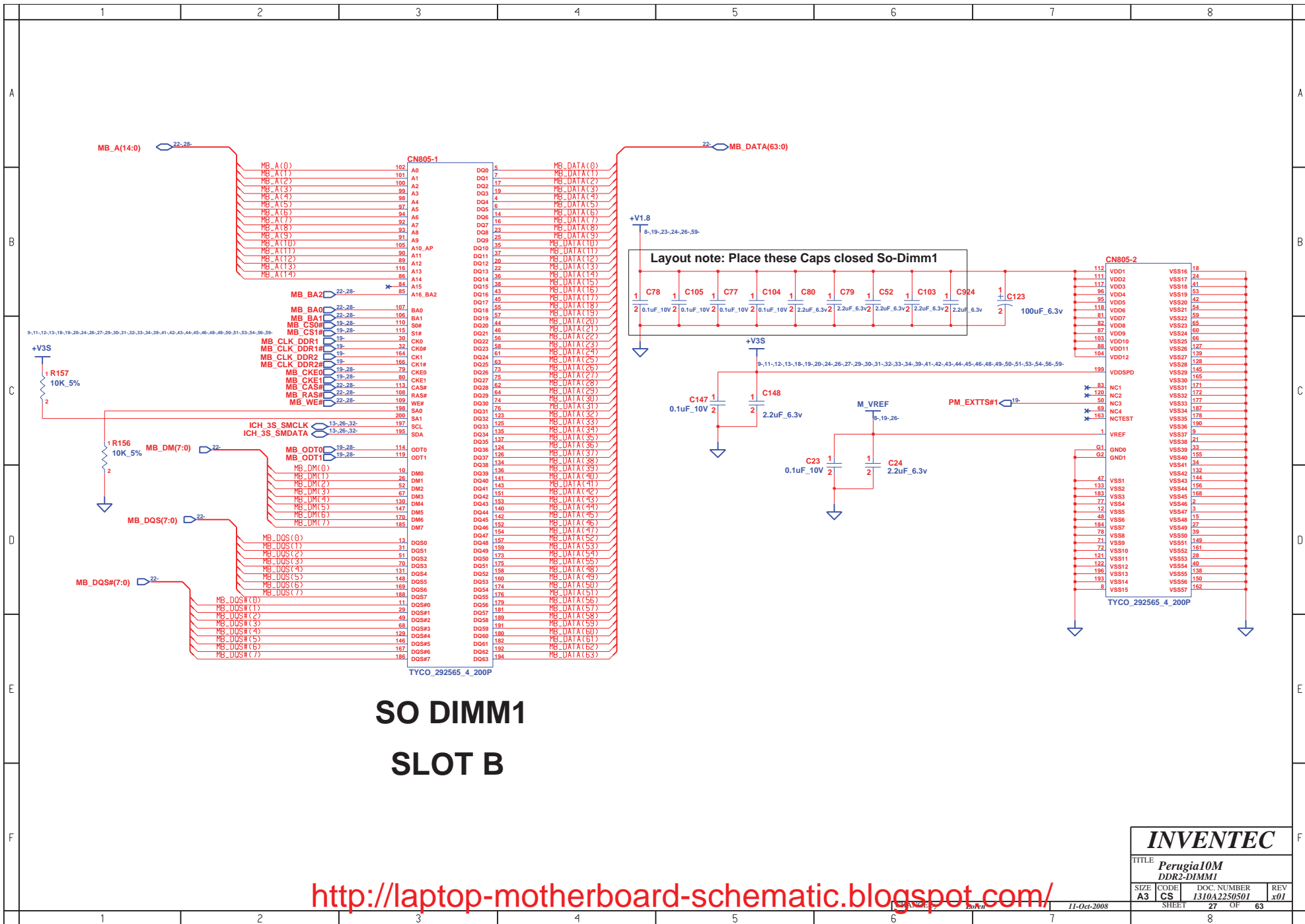
10-Oct-2008



**SO DIMM0
SLOT A**

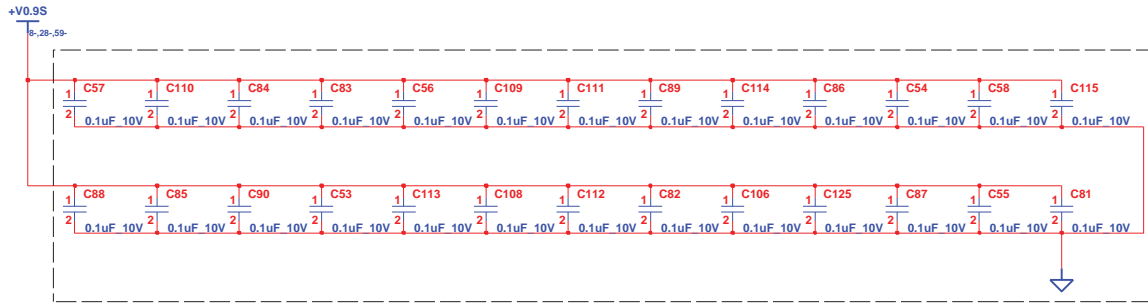
<http://laptop-motherboard-schematic.blogspot.com/>

INVENTEC			
TITLE Perugia10M DDR2-DIMM-0			
SIZE A3	CODE CS	DOC NUMBER 1310A2250501	REV x01
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10-Oct-2008			

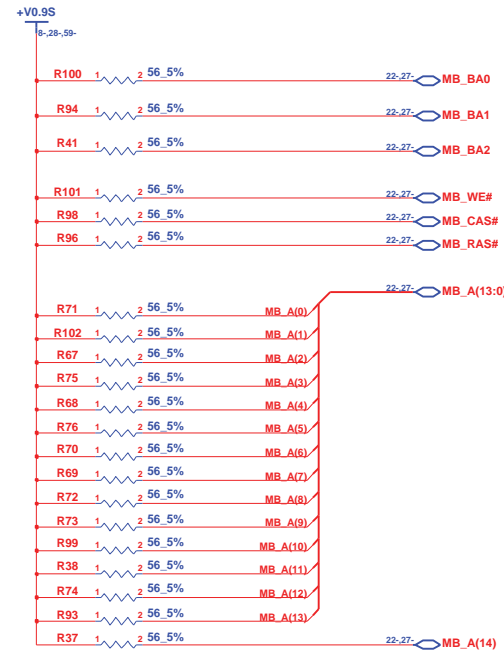
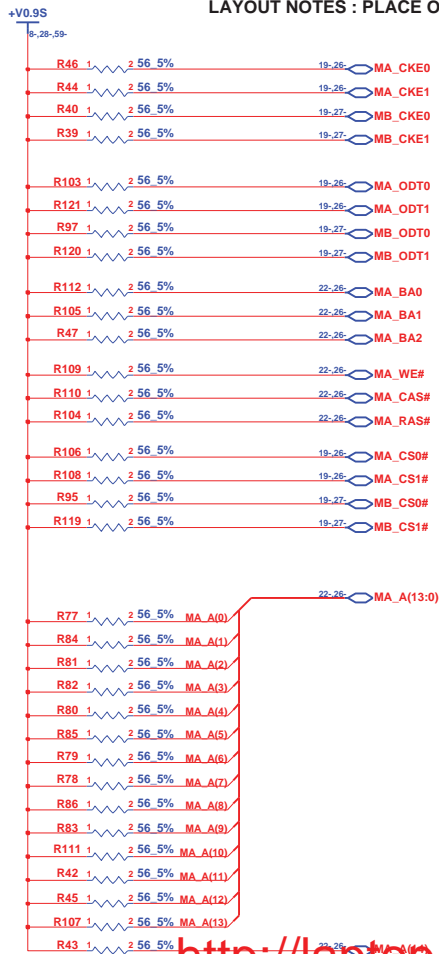


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INVENTEC			
TITLE Perugia10M DDR2-DIMM1			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	1310A2250501	x01
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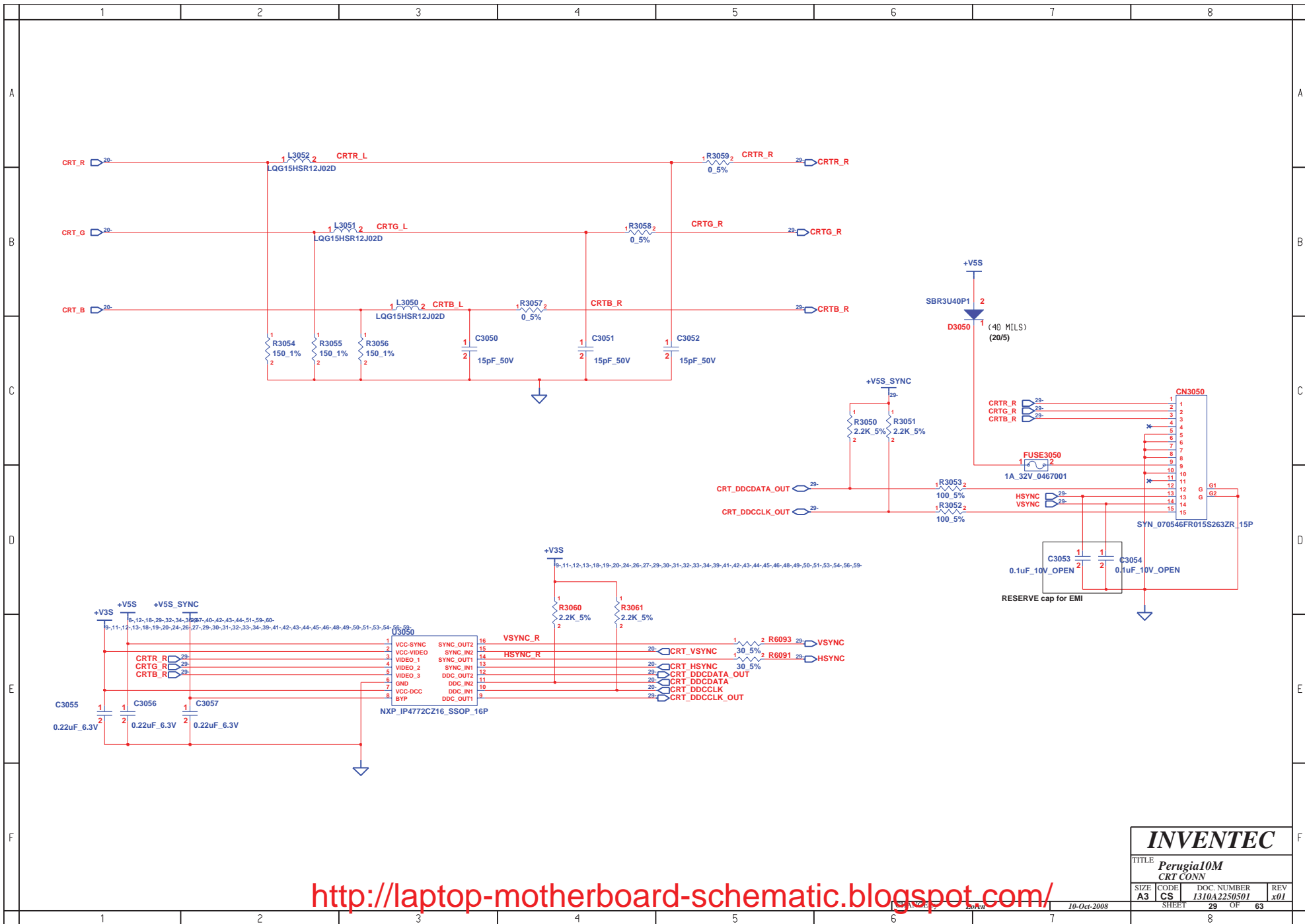
LAYOUT NOTES : PLACE ONE CAP CLOSE TO EVERY 2 PULL UP RESISTOR TERMINATED TO +V0.9S



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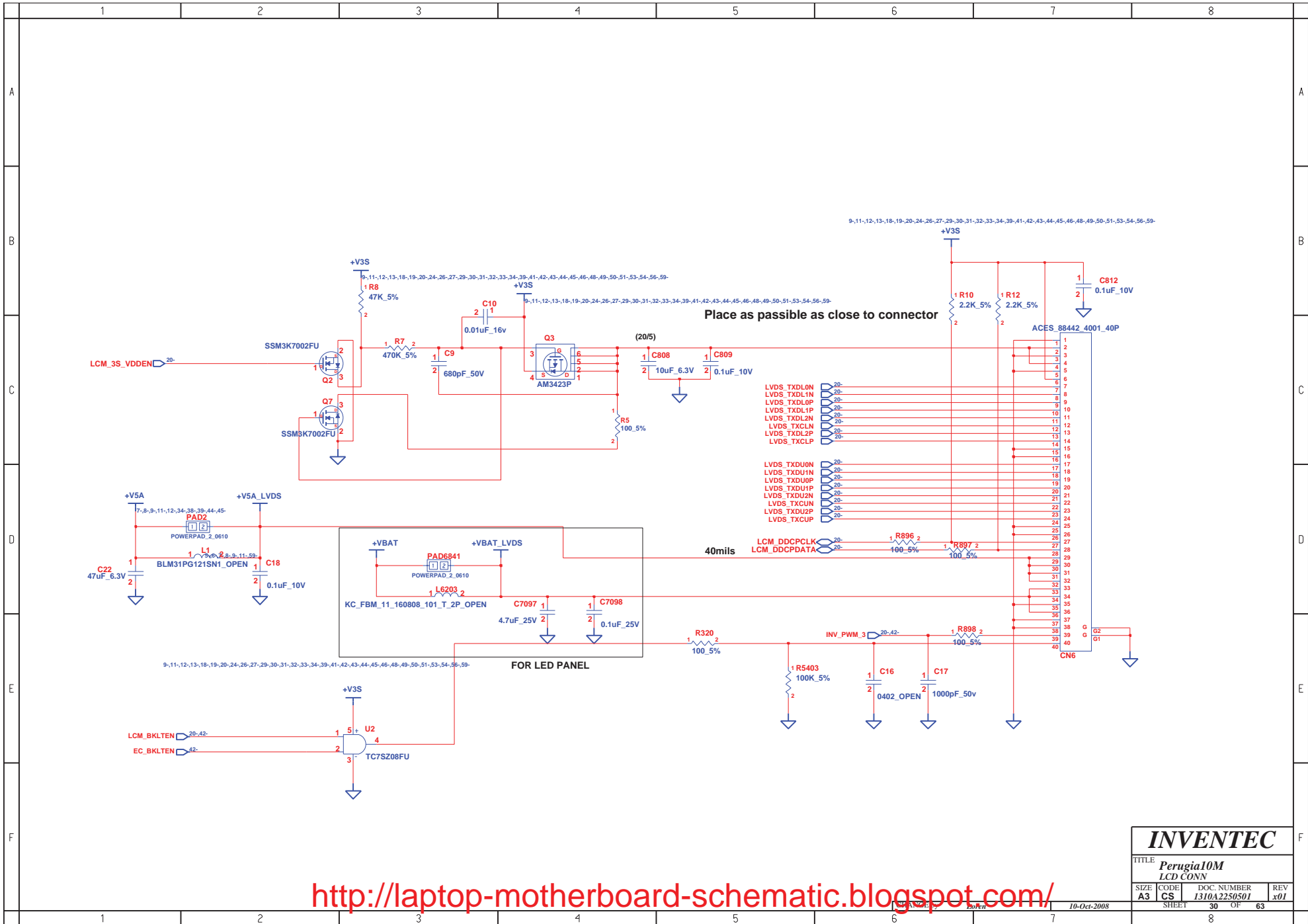
INVENTEC			
TITLE Perugia10M DDR2-DAMPING			
SIZE A3	CODE CS	DOC NUMBER 1310A2250501	REV x01
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27-Sep-2003



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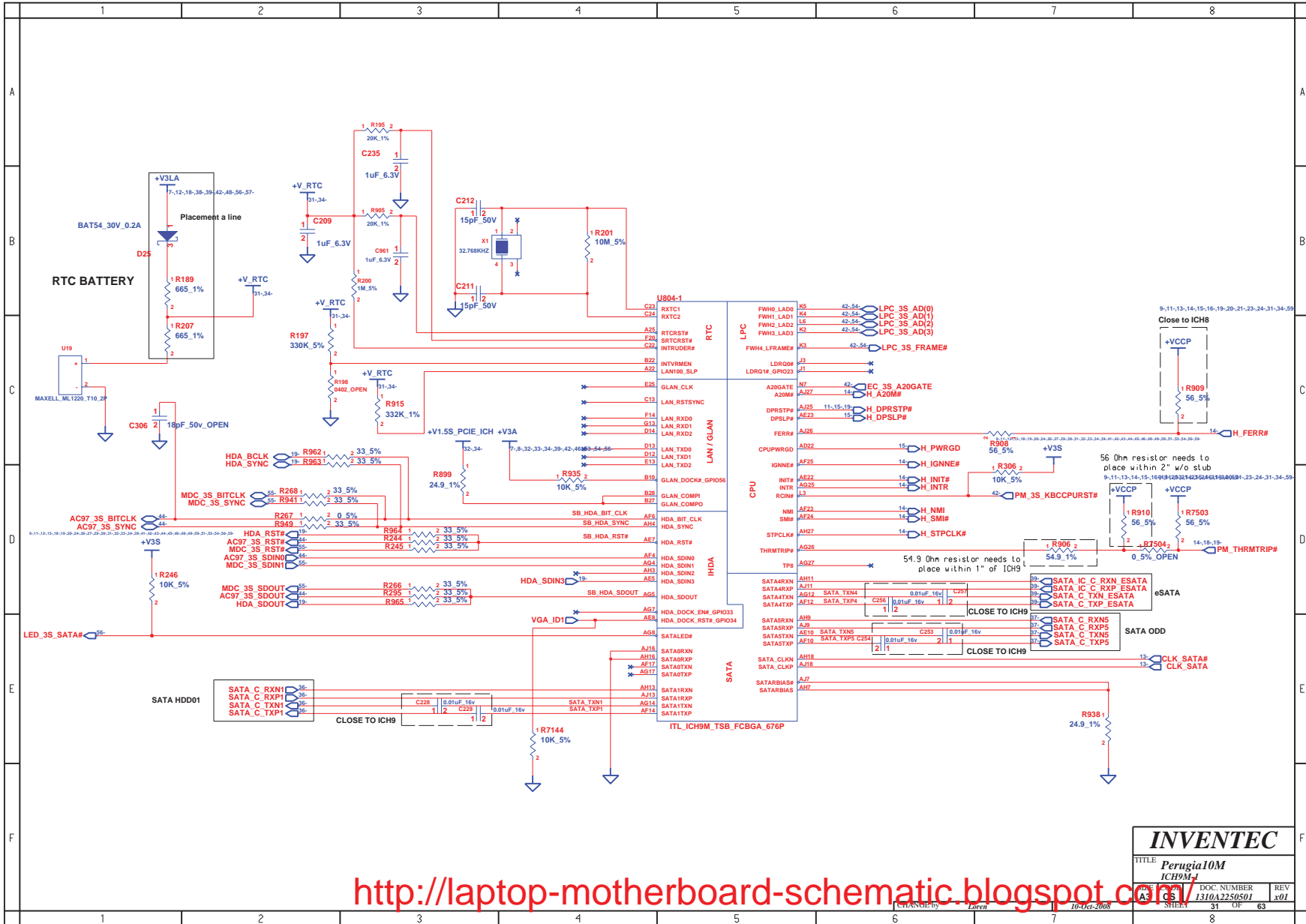
INVENTEC			
TITLE Perugia10M CRT CONN			
SIZE A3	CODE CS	DOC. NUMBER 1310A2250501	REV x01
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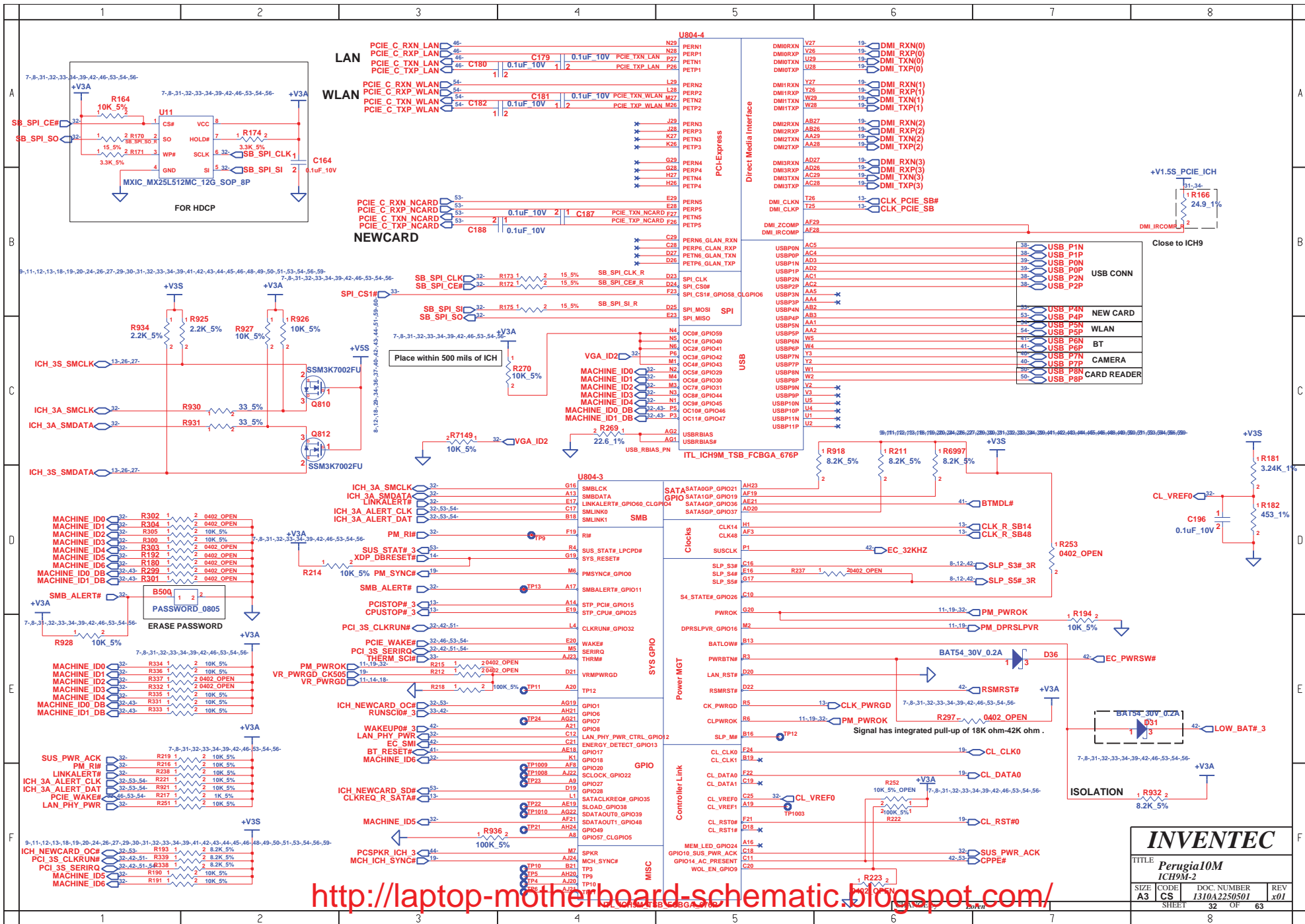
INVENTEC			
TITLE Perugia10M LCD CONN			
SIZE A3	CODE CS	DOC. NUMBER 1310A2250501	REV x01
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INVENTEC			
TITLE Perugia10M ICH9M-1			
DATE	REV	DOC NUMBER	REV
13/01/08	CS	1310A2250501	x01
DRAWN BY Loren		SHEET 31 OF 63	

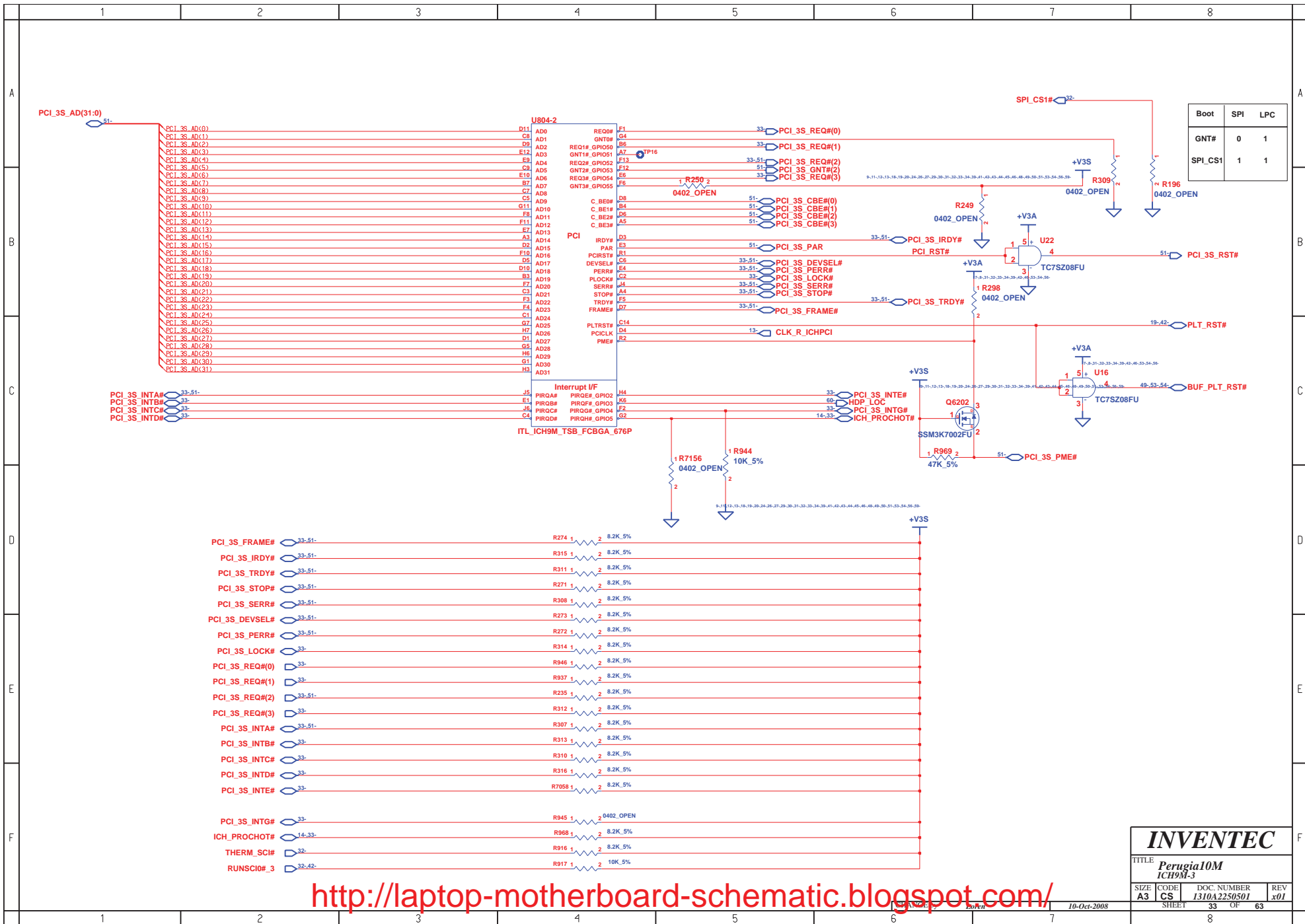


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INVENTEC

TITLE
Perugia10M
ICH9M-2

SIZE A3	CODE CS	DOC. NUMBER 1310A2250501	REV x01
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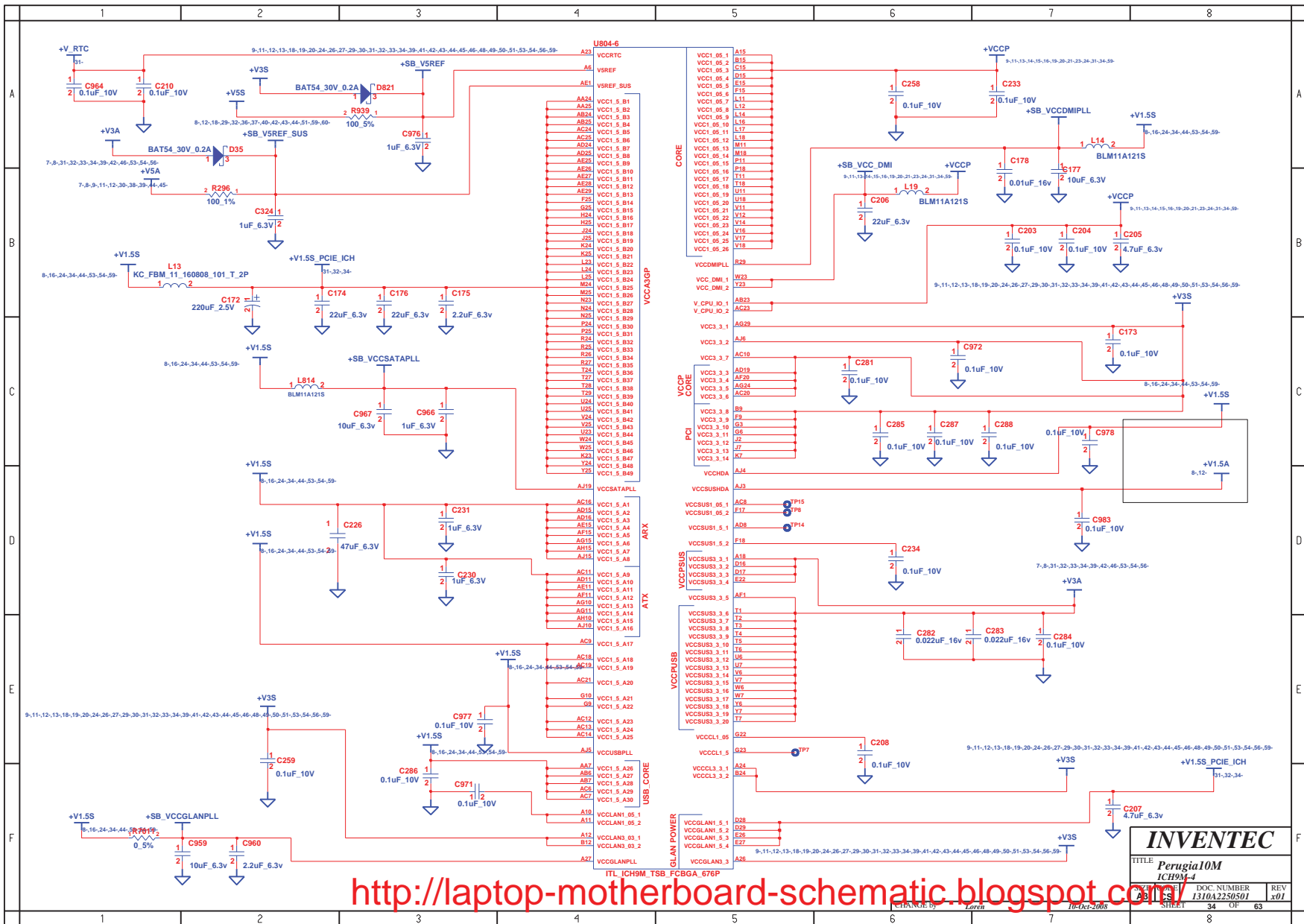


Boot	SPI	LPC
GNT#	0	1
SPI_CS1	1	1

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INVENTEC			
TITLE Perugia10M ICh9M-3			
SIZE A3	CODE CS	DOC. NUMBER 1310A2250501	REV x01
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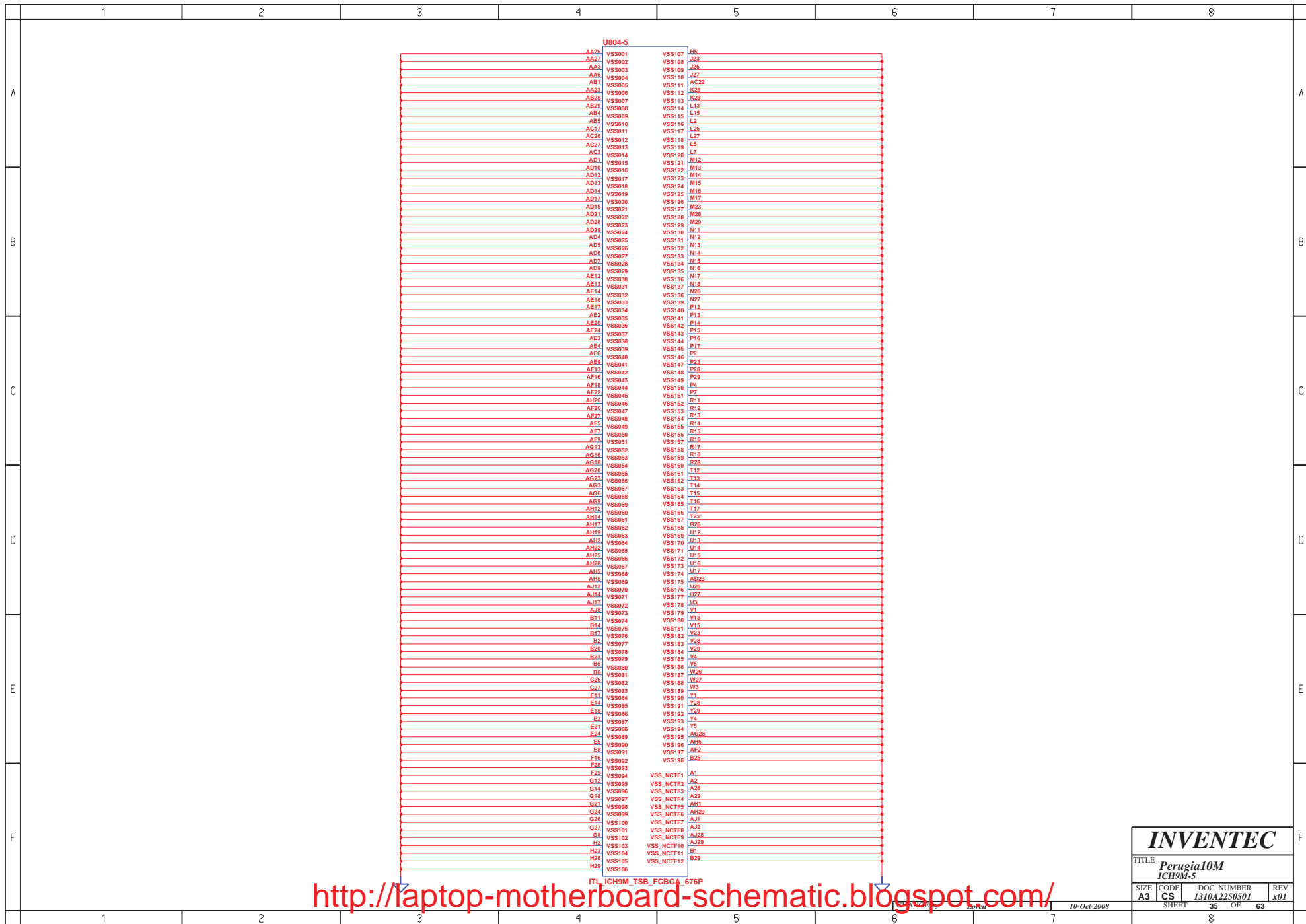
INVENTEC

TITLE: **Perugina ICH9M-4**

REV	DOC NUMBER
x01	1310A2250501

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DATE: 10-Dec-2008
 DRAWN BY: Loren



U804-5

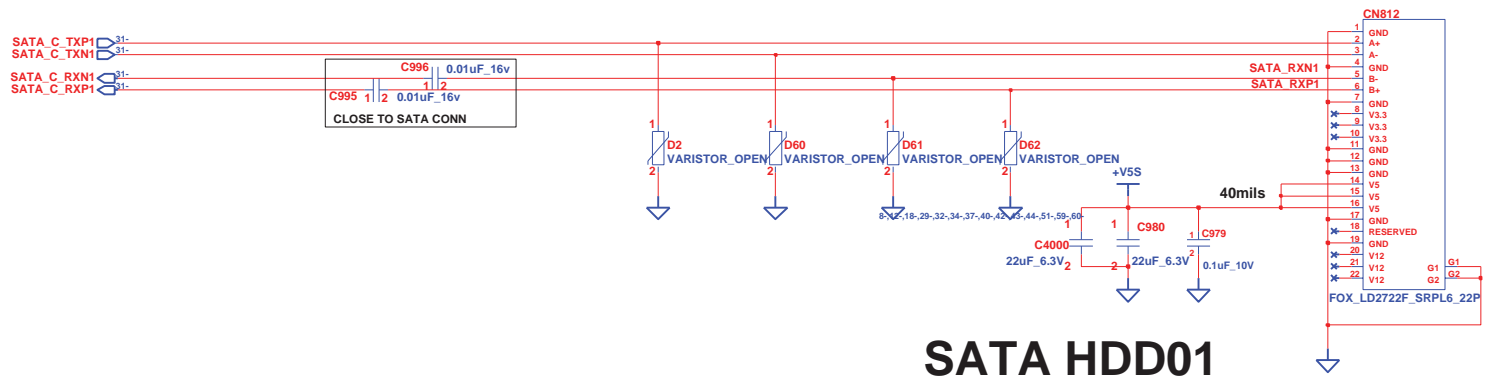
- AA26 VSS001
- AA27 VSS002
- AA3 VSS003
- AA4 VSS004
- AB1 VSS005
- AA23 VSS006
- AB28 VSS007
- AB29 VSS008
- AB4 VSS009
- AB5 VSS010
- AC11 VSS011
- AC26 VSS012
- AC27 VSS013
- AC3 VSS014
- AD1 VSS015
- AD10 VSS016
- AD12 VSS017
- AD13 VSS018
- AD14 VSS019
- AD17 VSS020
- AD18 VSS021
- AD21 VSS022
- AD28 VSS023
- AD29 VSS024
- AD4 VSS025
- AD5 VSS026
- AD6 VSS027
- AD7 VSS028
- AD9 VSS029
- AE13 VSS030
- AE13 VSS031
- AE14 VSS032
- AE14 VSS033
- AE17 VSS034
- AE3 VSS035
- AE20 VSS036
- AE24 VSS037
- AE3 VSS038
- AE4 VSS039
- AEE VSS040
- AEB VSS041
- AF13 VSS042
- AF16 VSS043
- AF18 VSS044
- AF22 VSS045
- AH26 VSS046
- AF28 VSS047
- AF27 VSS048
- AF3 VSS049
- AF7 VSS050
- AF9 VSS051
- AG13 VSS052
- AG16 VSS053
- AG18 VSS054
- AG20 VSS055
- AG23 VSS056
- AG3 VSS057
- AG5 VSS058
- AG9 VSS059
- AH12 VSS060
- AH14 VSS061
- AH17 VSS062
- AH19 VSS063
- AHR VSS064
- AH22 VSS065
- AH28 VSS066
- AHR VSS067
- AHE VSS068
- AJ12 VSS069
- AJ14 VSS070
- AJ17 VSS071
- AJ8 VSS072
- B11 VSS073
- B14 VSS074
- B17 VSS075
- B3 VSS076
- B20 VSS077
- B23 VSS078
- B5 VSS079
- B8 VSS080
- C28 VSS081
- C27 VSS082
- E11 VSS083
- E14 VSS084
- E18 VSS085
- ER VSS086
- ER VSS087
- E21 VSS088
- E24 VSS089
- E5 VSS090
- ER VSS091
- F16 VSS092
- F28 VSS093
- F29 VSS094
- G12 VSS095
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- H29 VSS105
- H29 VSS106
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- VSS116 L2
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- VSS119 L5
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- VSS124 M15
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- VSS126 M17
- VSS127 M23
- VSS128 M28
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- VSS170 U13
- VSS171 U14
- VSS172 U15
- VSS173 U16
- VSS174 U17
- VSS175 AD23
- VSS176 U26
- VSS177 U27
- VSS178 U3
- VSS179 V1
- VSS180 V13
- VSS181 V15
- VSS182 V23
- VSS183 V28
- VSS184 V29
- VSS185 V4
- VSS186 V5
- VSS187 W26
- VSS188 W27
- VSS189 W3
- VSS190 Y1
- VSS191 Y28
- VSS192 Y29
- VSS193 Y4
- VSS194 Y5
- VSS195 AG28
- VSS196 AH6
- VSS197 AF2
- VSS198 B25
- VSS.NCTF1 A1
- VSS.NCTF2 A2
- VSS.NCTF3 A28
- VSS.NCTF4 A29
- VSS.NCTF5 AH1
- VSS.NCTF6 AH29
- VSS.NCTF7 AJ1
- VSS.NCTF8 AJ2
- VSS.NCTF9 AJ28
- VSS.NCTF10 AJ29
- VSS.NCTF11 B1
- VSS.NCTF12 B29

ITL ICH9M_TSB_FCBGA_676P

<http://laptop-motherboard-schematic.blogspot.com/>

INVENTEC			
TITLE <i>Perugia10M ICH9M-5</i>			
SIZE A3	CODE CS	DOC. NUMBER <i>1310A2250501</i>	REV x01
SHEET		35	OF 63

10-Oct-2008

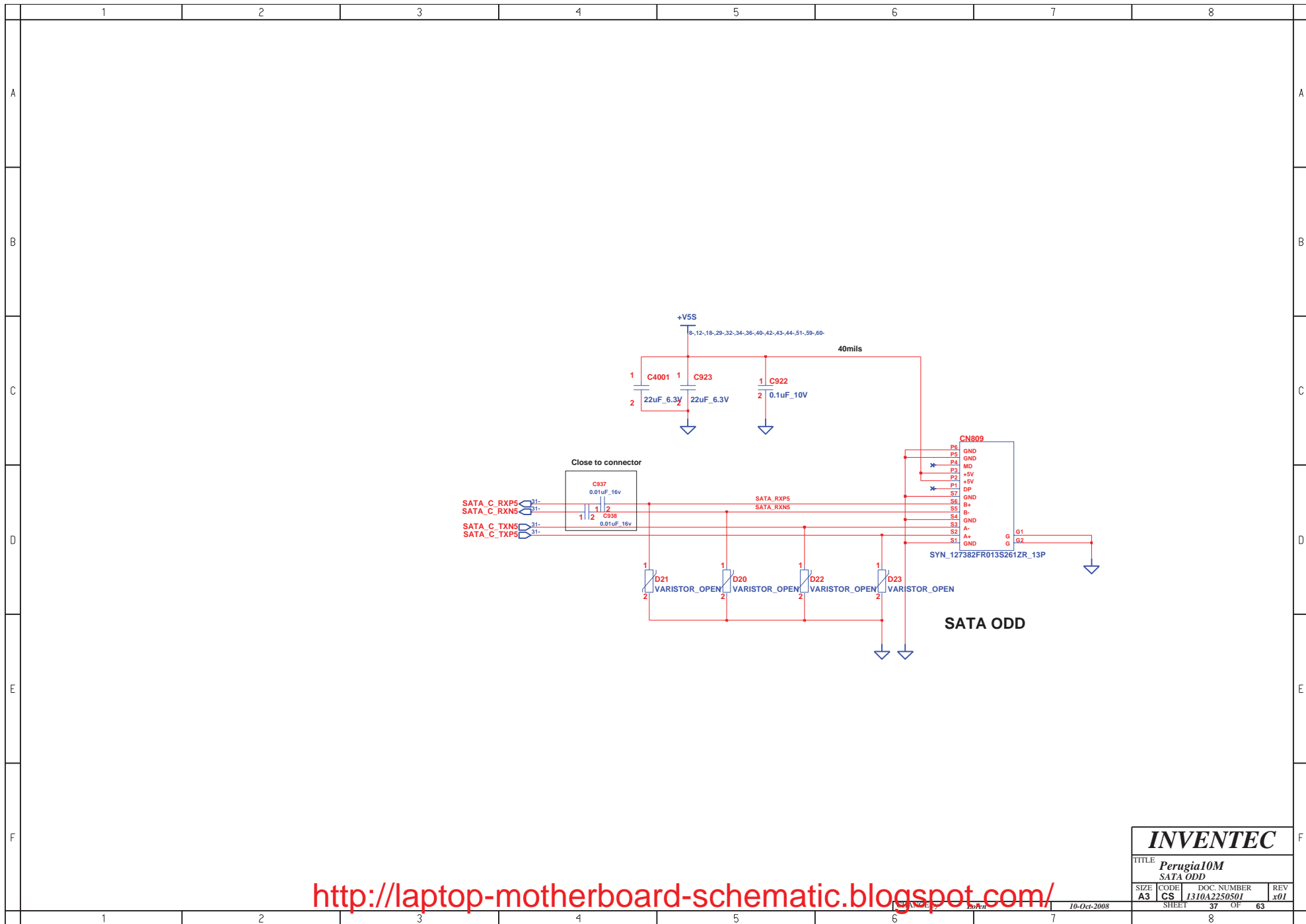


SATA HDD01

<http://laptop-motherboard-schematic.blogspot.com/>

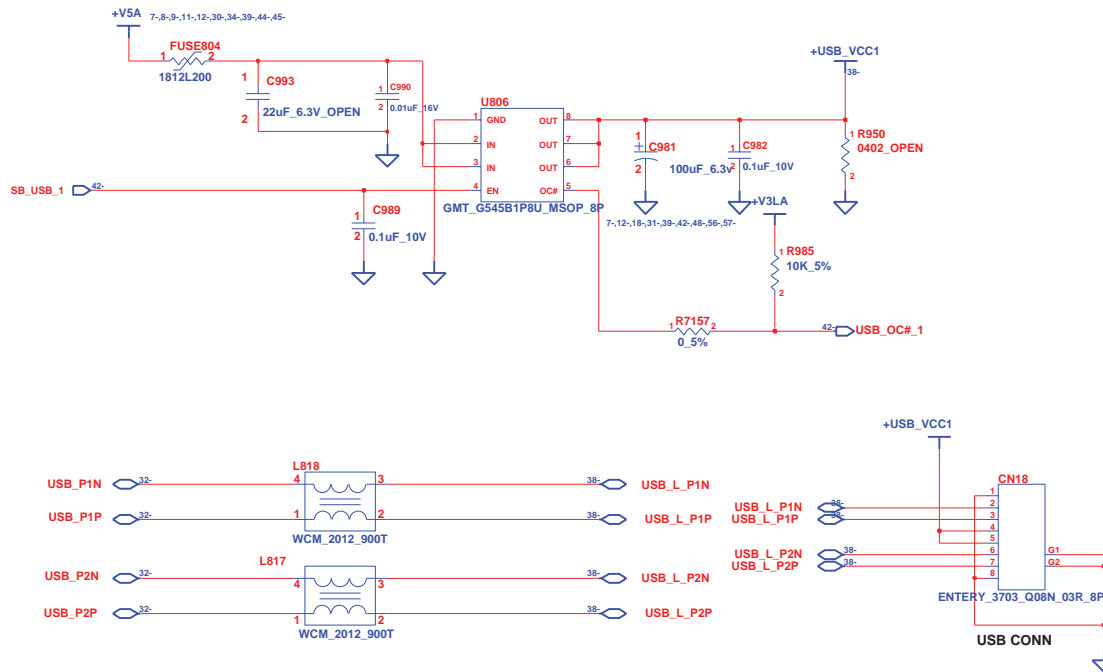
INVENTEC			
TITLE Perugia10M HDD CONN			
SIZE A3	CODE CS	DOC. NUMBER 1310A2250501	REV x01
SHEET		36	OF 63

11-Oct-2008



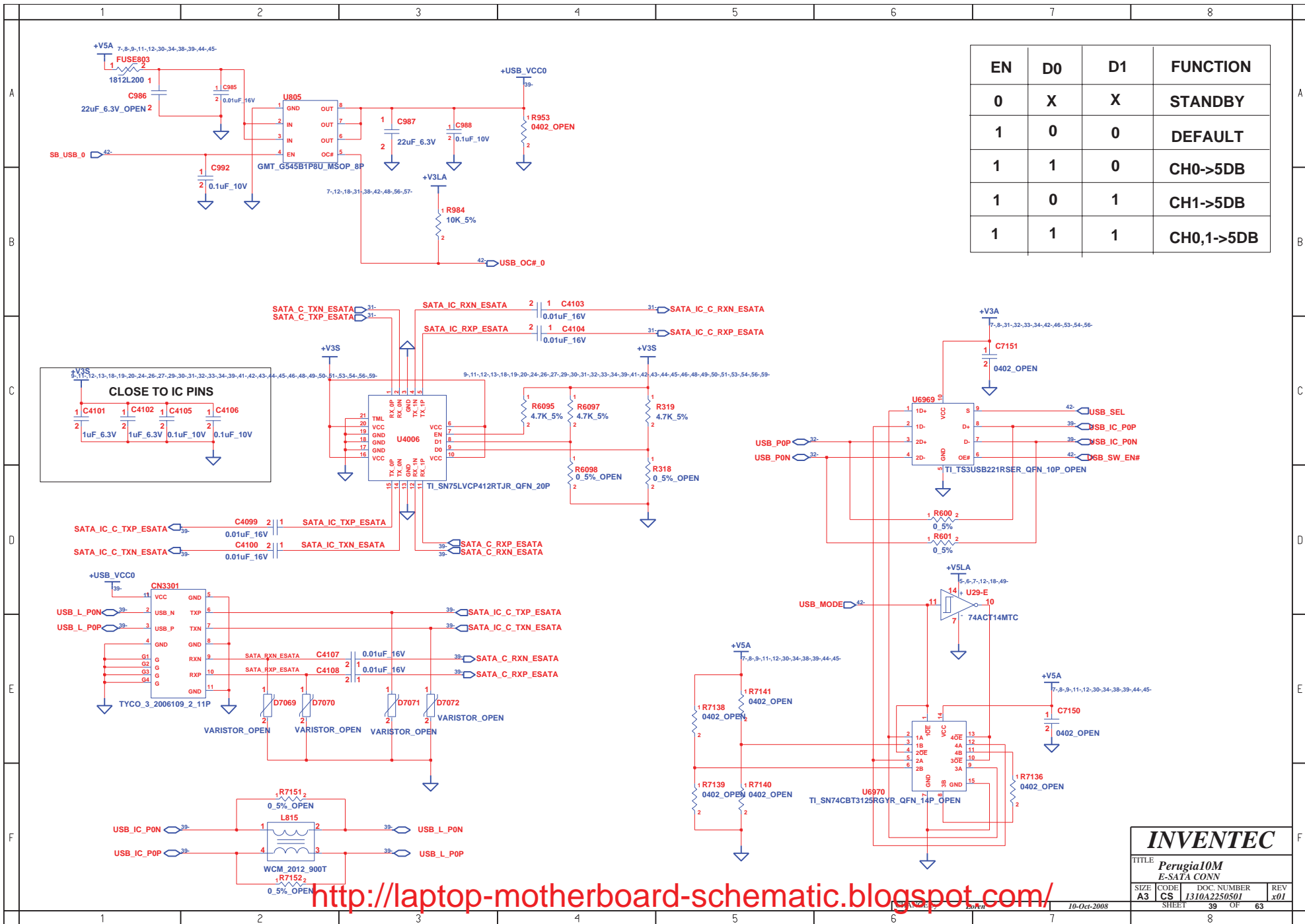
<http://laptop-motherboard-schematic.blogspot.com/>

INVENTEC			
TITLE Perugia10M SATA ODD			
SIZE A3	CODE CS	DOC. NUMBER 1310A2250501	REV x01
10-Oct-2008		SHEET 37	OF 63



<http://laptop-motherboard-schematic.blogspot.com/>

INVENTEC			
TITLE Perugia10M USB CONN/USB(DB)			
SIZE A3	CODE CS	DOC. NUMBER 1310A2250501	REV x01
DATE 10-Oct-2008		SHEET 38 OF 63	

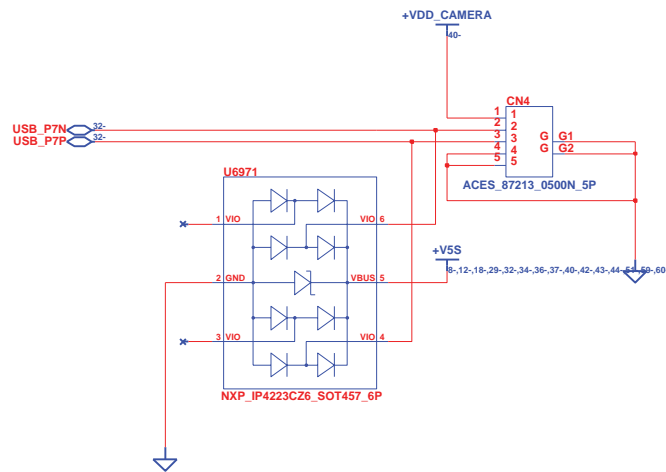
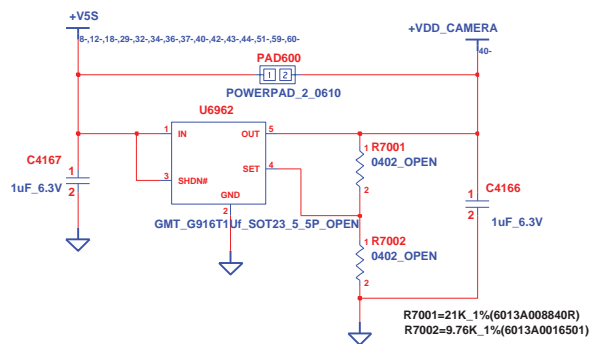


EN	D0	D1	FUNCTION
0	X	X	STANDBY
1	0	0	DEFAULT
1	1	0	CH0->5DB
1	0	1	CH1->5DB
1	1	1	CH0,1->5DB

INVENTEC			
TITLE <i>Perugia10M</i> E-SATA CONN			
SIZE A3	CODE CS	DOC NUMBER I310A2250501	REV x01
SHEET 39		OF 63	

<http://laptop-motherboard-schematic.blogspot.com/>

10-Oct-2008

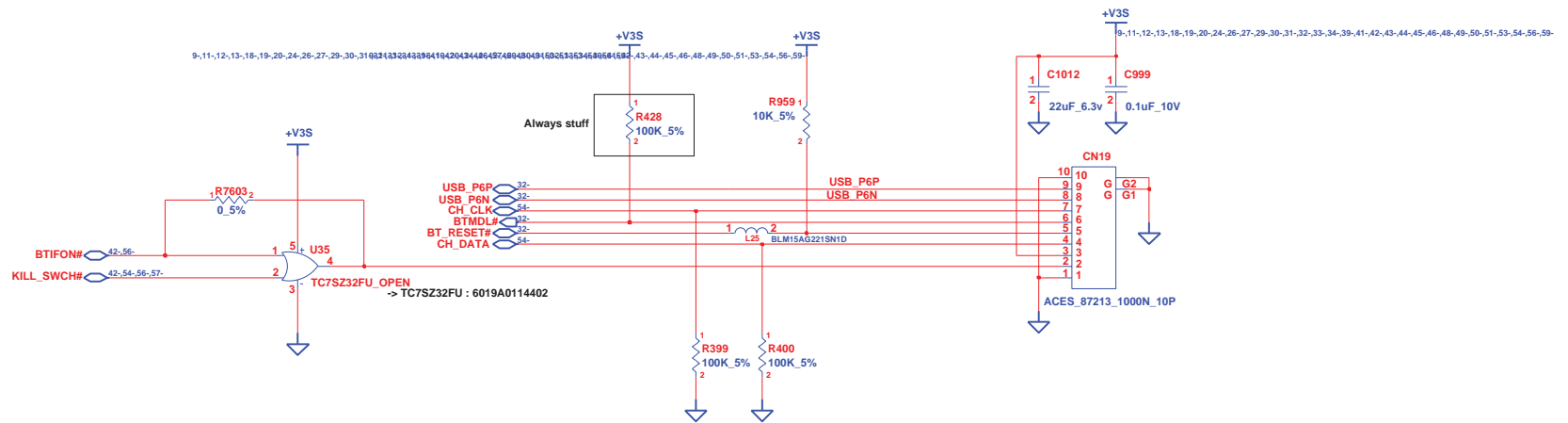


<http://laptop-motherboard-schematic.blogspot.com/>

INVENTEC			
TITLE Perugia10M Camera			
SIZE A3	CODE CS	DOC. NUMBER 1310A2250501	REV x01
SHEET		40	OF 63

10-Oct-2008

8

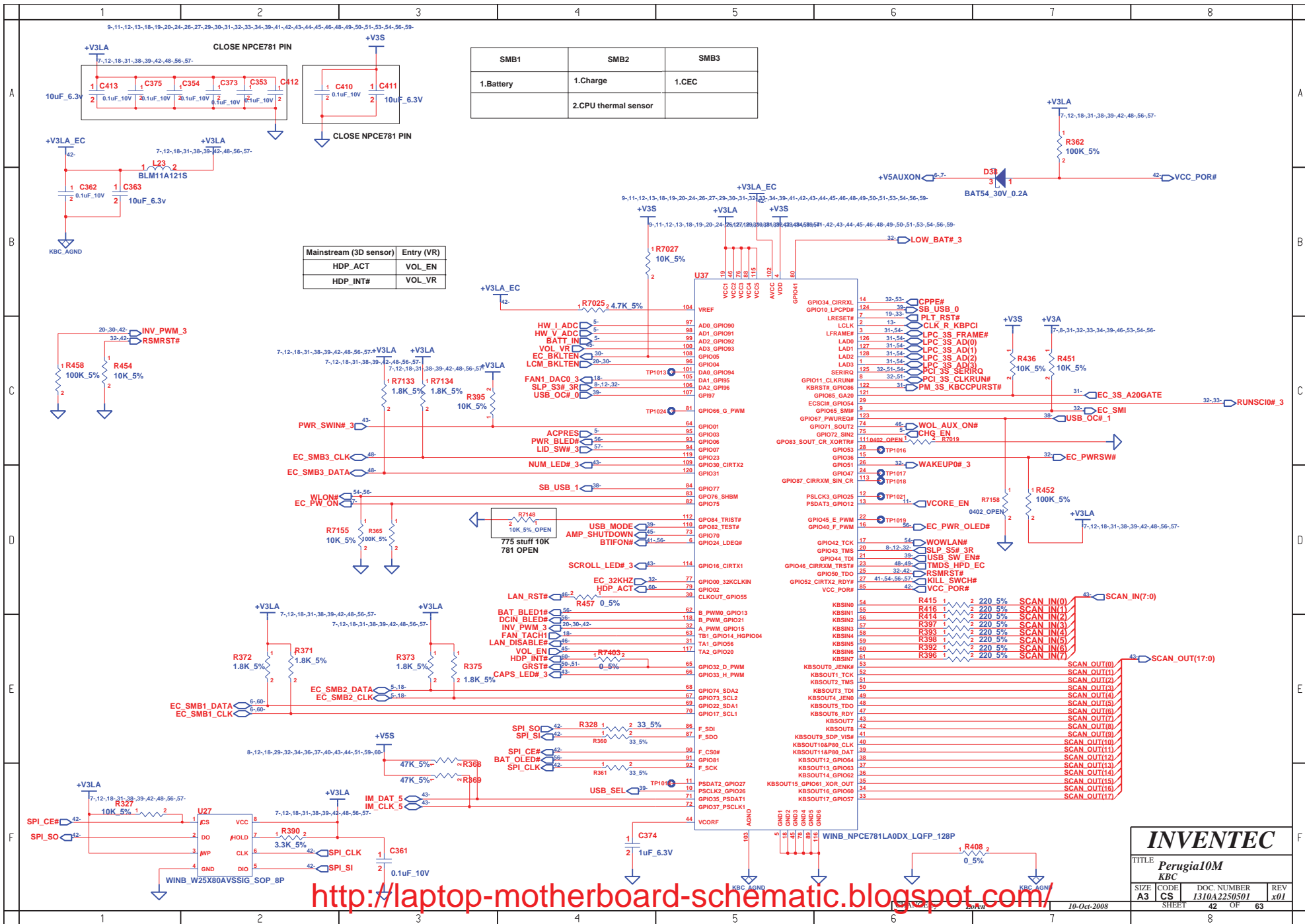


<http://laptop-motherboard-schematic.blogspot.com/>

INVENTEC			
TITLE Perugia10M Bluetooth			
SIZE A3	CODE CS	DOC. NUMBER 1310A2250501	REV x01
SHEET		41	OF 63

10-Oct-2008

8



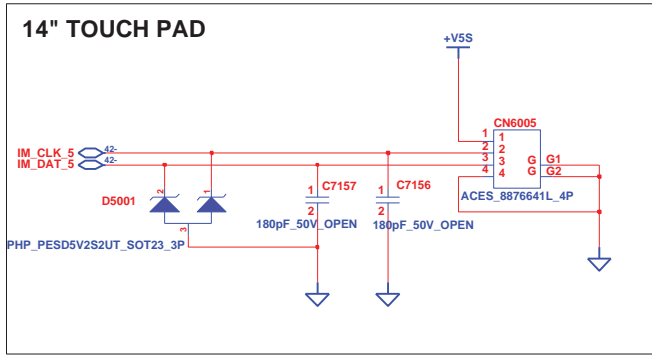
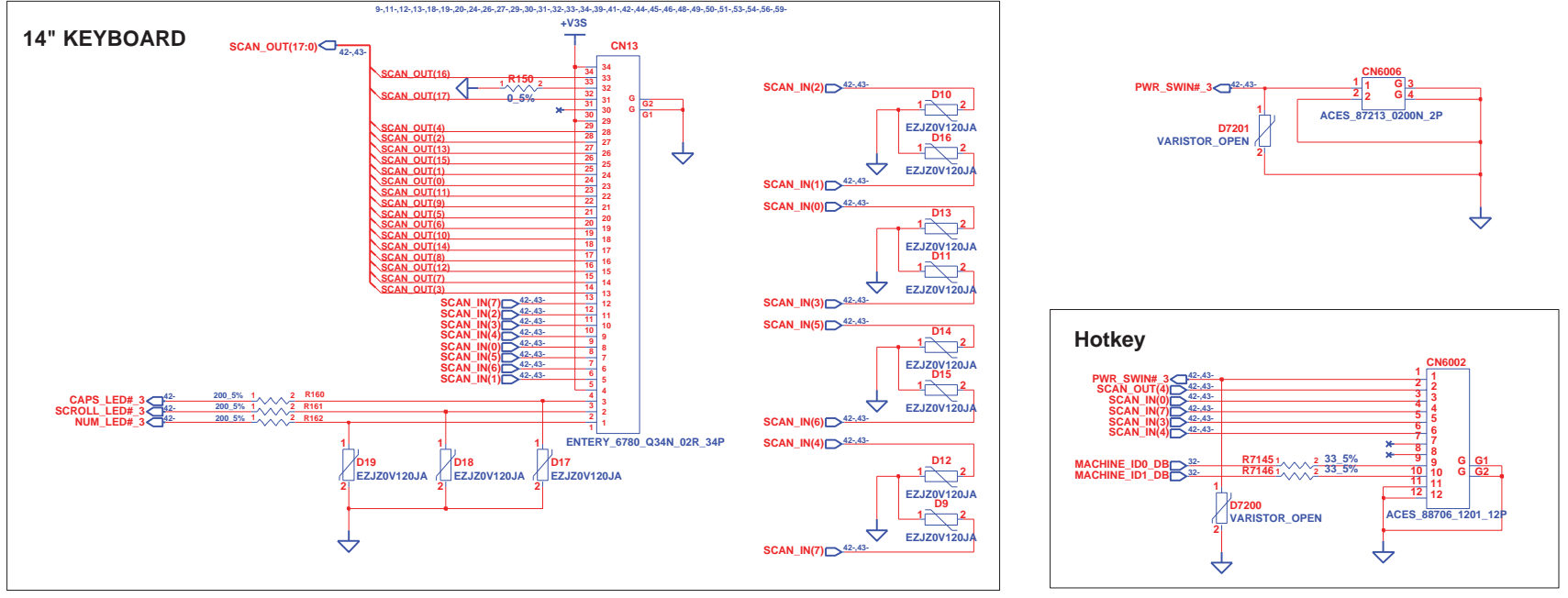
<http://laptop-motherboard-schematic.blogspot.com/>

INVENTEC

TITLE: **Perugia10M KBC**

SIZE	CODE	DOC. NUMBER	REV
A3	CS	1310A2250501	x01

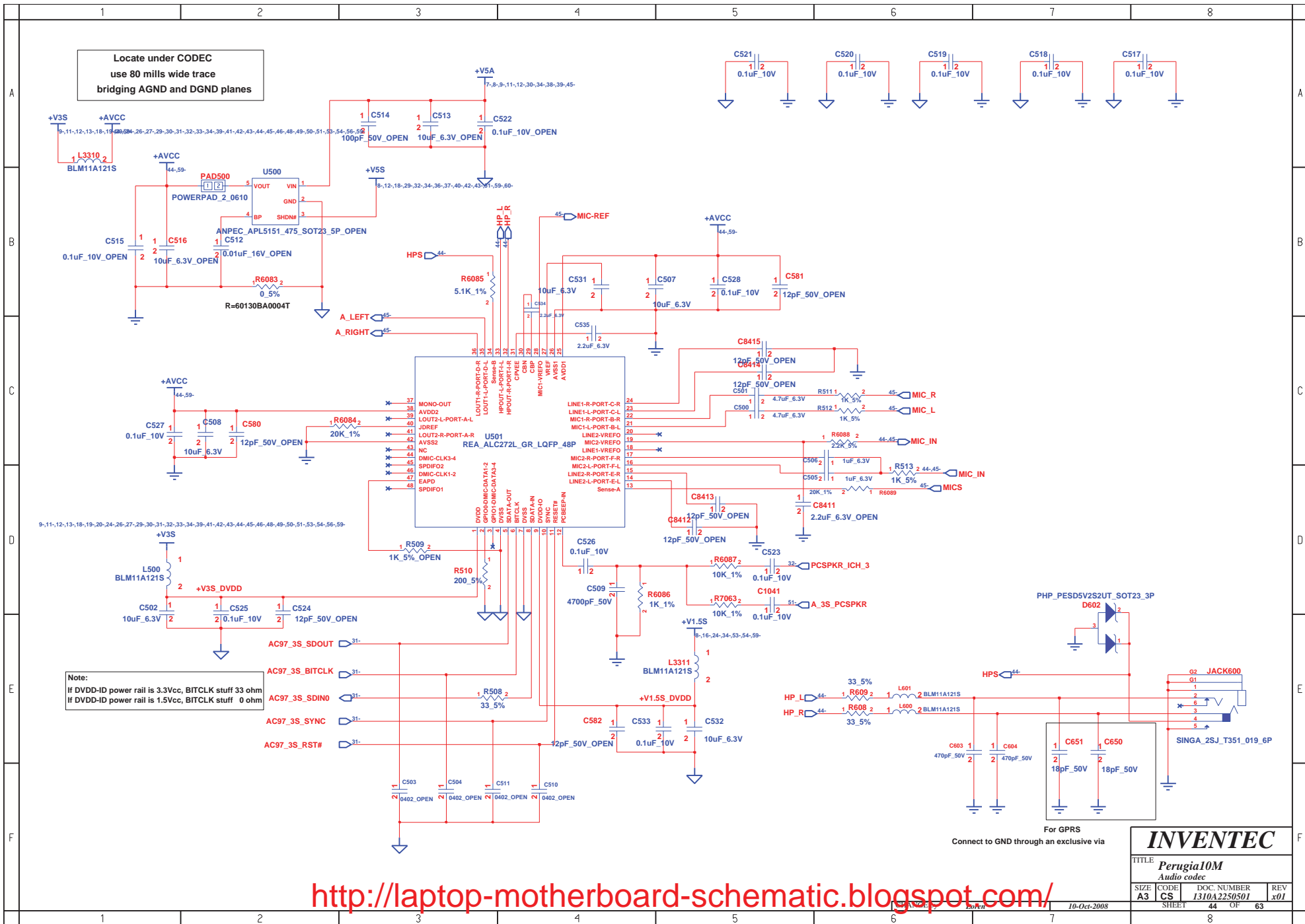
10-Oct-2008 SHEET 42 OF 63



<http://laptop-motherboard-schematic.blogspot.com/>

INVENTEC			
TITLE <i>Perugia10M</i> K/B & TP/B CONN			
SIZE A3	CODE CS	DOC. NUMBER 1310A2250501	REV x01
SHEET 43		OF 63	

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Locate under CODEC
use 80 mills wide trace
bridging AGND and DGND planes

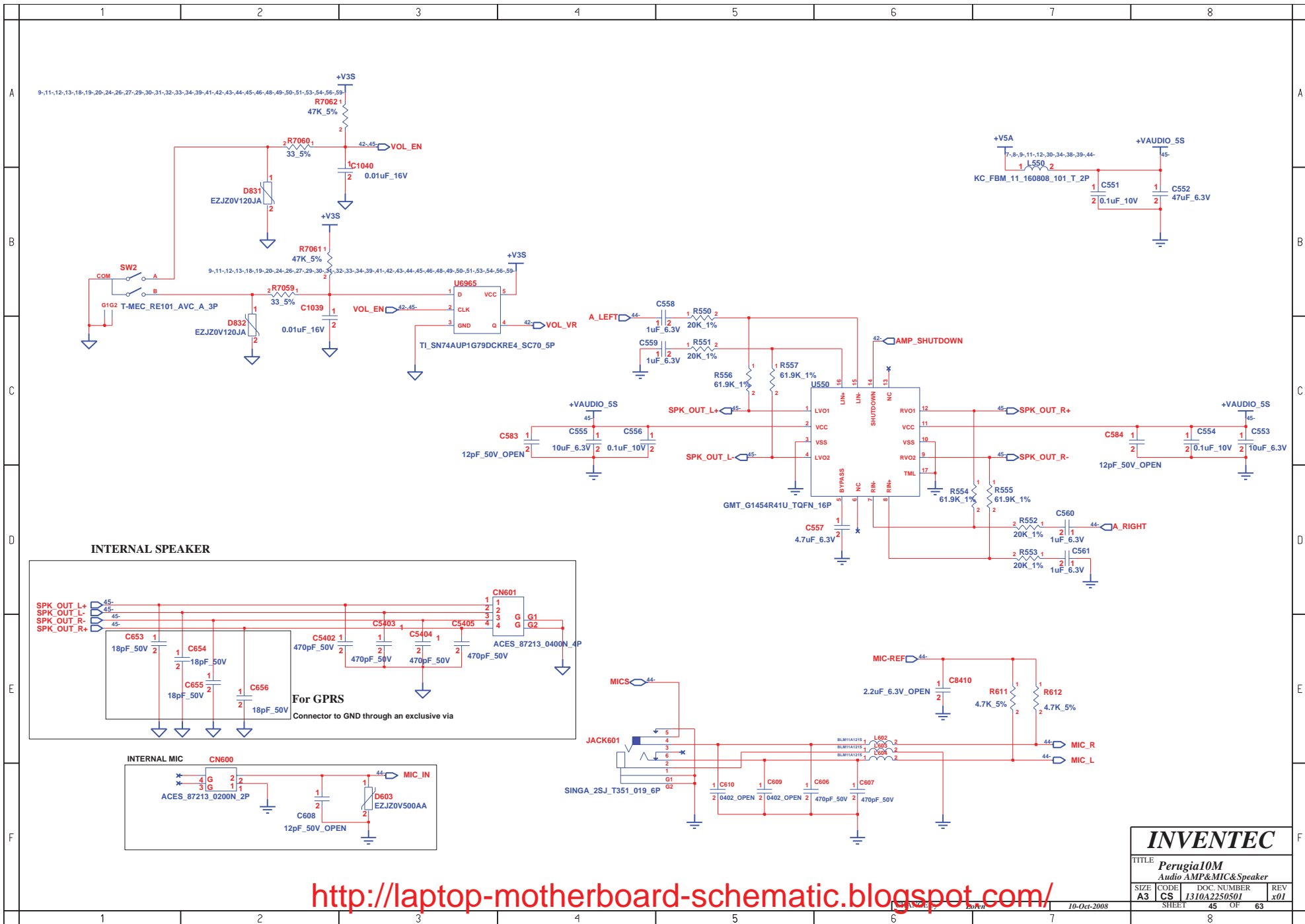
Note:
If DVDD-ID power rail is 3.3Vcc, BITCLK stuff 33 ohm
If DVDD-ID power rail is 1.5Vcc, BITCLK stuff 0 ohm

For GPRS
Connect to GND through an exclusive via

INVENTEC			
TITLE <i>Perugia10M</i> Audio codec			
SIZE A3	CODE CS	DOC. NUMBER 1310A2250501	REV x01
SHEET 44		OF 63	

<http://laptop-motherboard-schematic.blogspot.com/>

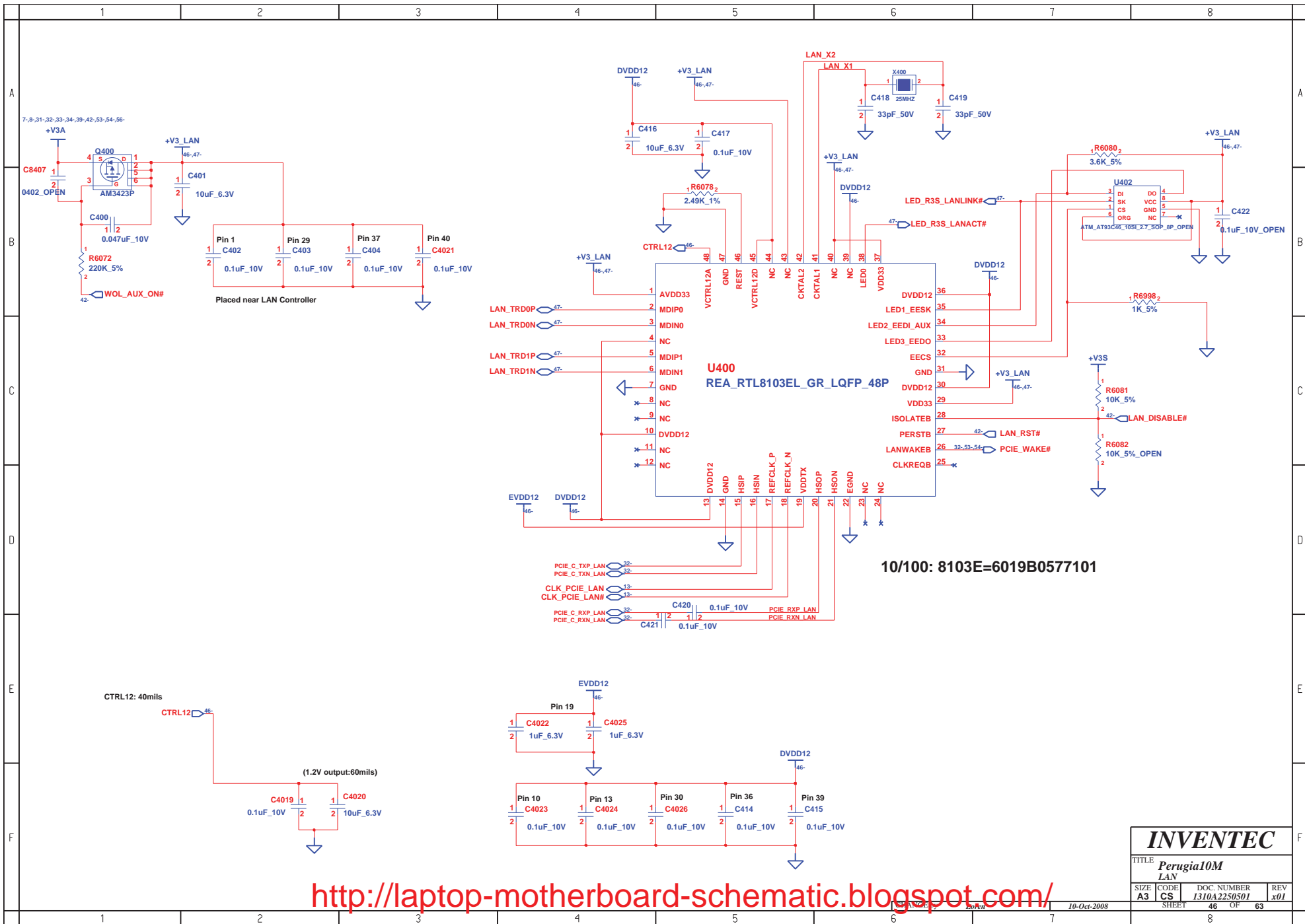
10-Oct-2008



<http://laptop-motherboard-schematic.blogspot.com/>

INVENTEC			
TITLE Perugia10M Audio AMP&MIC&Speaker			
SIZE A3	CODE CS	DOC NUMBER I310A2250501	REV x01
SHEET 45		OF 63	

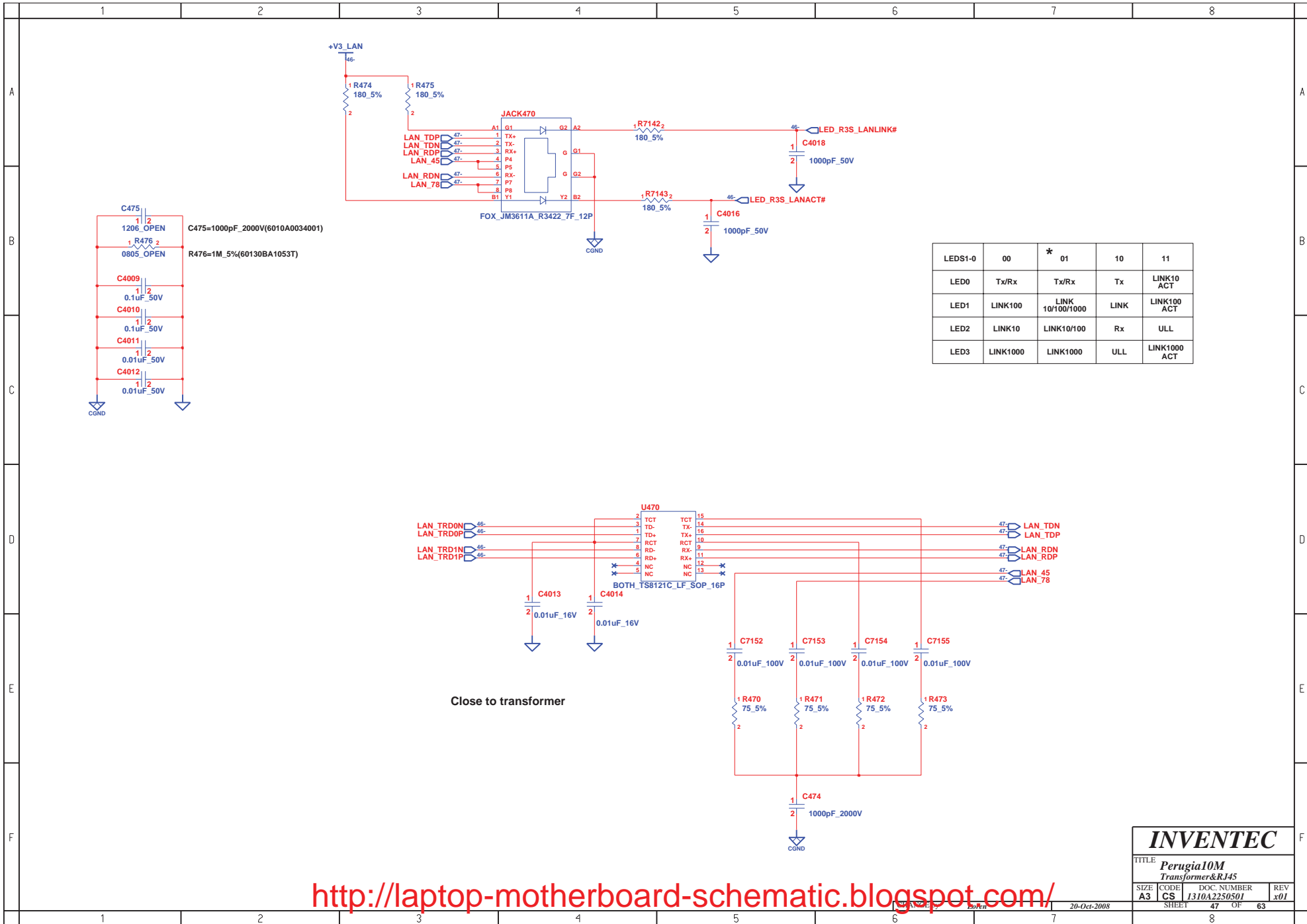
10-Oct-2008



<http://laptop-motherboard-schematic.blogspot.com/>

INVENTEC			
TITLE Perugia10M LAN			
SIZE A3	CODE CS	DOC. NUMBER 1310A2250501	REV x01
SHEET 46		OF 63	

10-Oct-2008



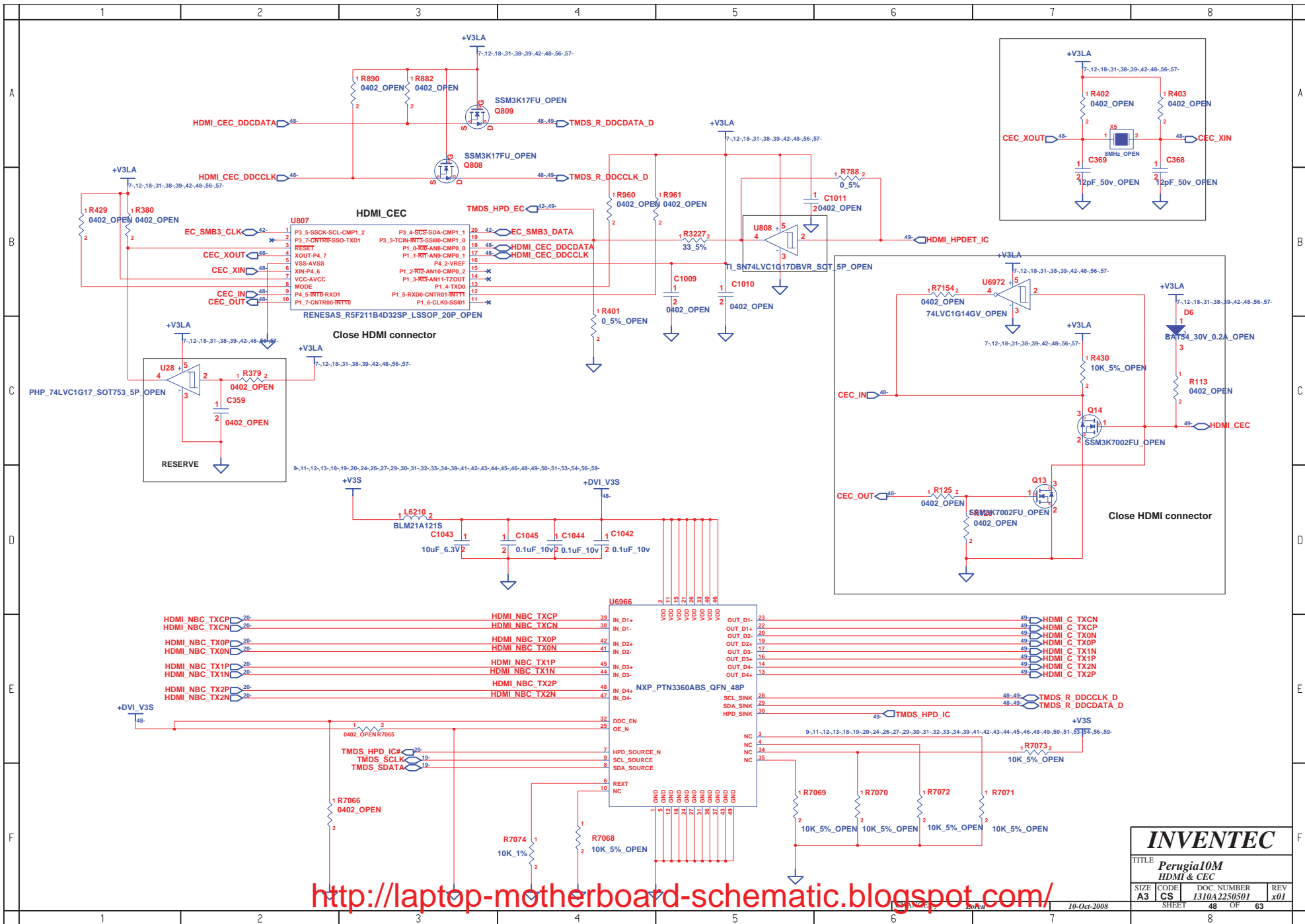
LEDS1-0	00	* 01	10	11
LED0	Tx/Rx	Tx/Rx	Tx	LINK10 ACT
LED1	LINK100	LINK 10/100/1000	LINK	LINK100 ACT
LED2	LINK10	LINK10/100	Rx	ULL
LED3	LINK1000	LINK1000	ULL	LINK1000 ACT

INVENTEC

TITLE
Perugia10M
 Transformer&RJ45

SIZE	CODE	DOC. NUMBER	REV
A3	CS	1310A2250501	x01

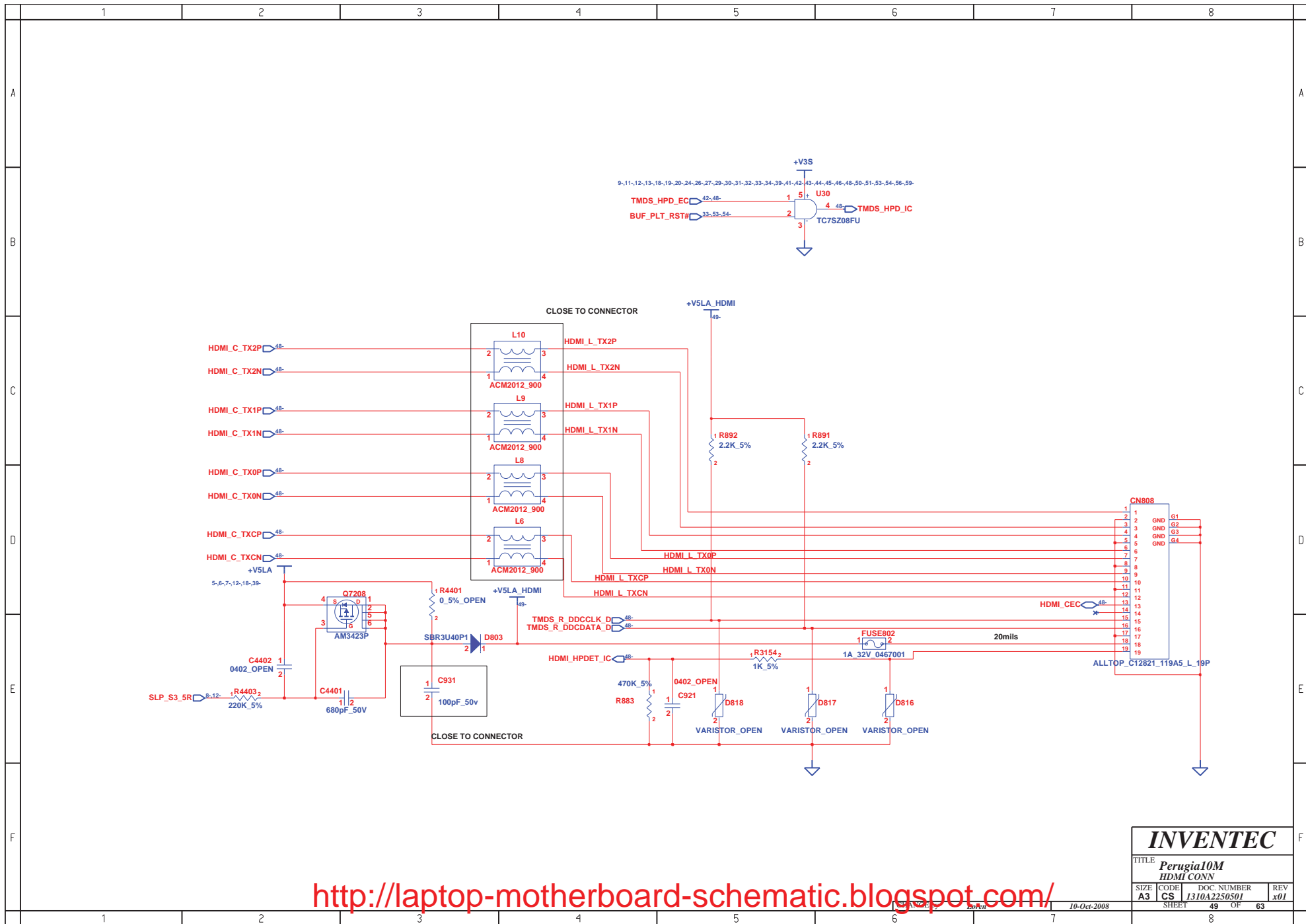
<http://laptop-motherboard-schematic.blogspot.com/>



<http://laptop-motherboard-schematic.blogspot.com/>

INVENTEC			
TITLE Perugia10M HDMI & CEC			
SIZE A3	CODE CS	DOC. NUMBER 1310A2250501	REV x01
SHEET 48		OF 63	

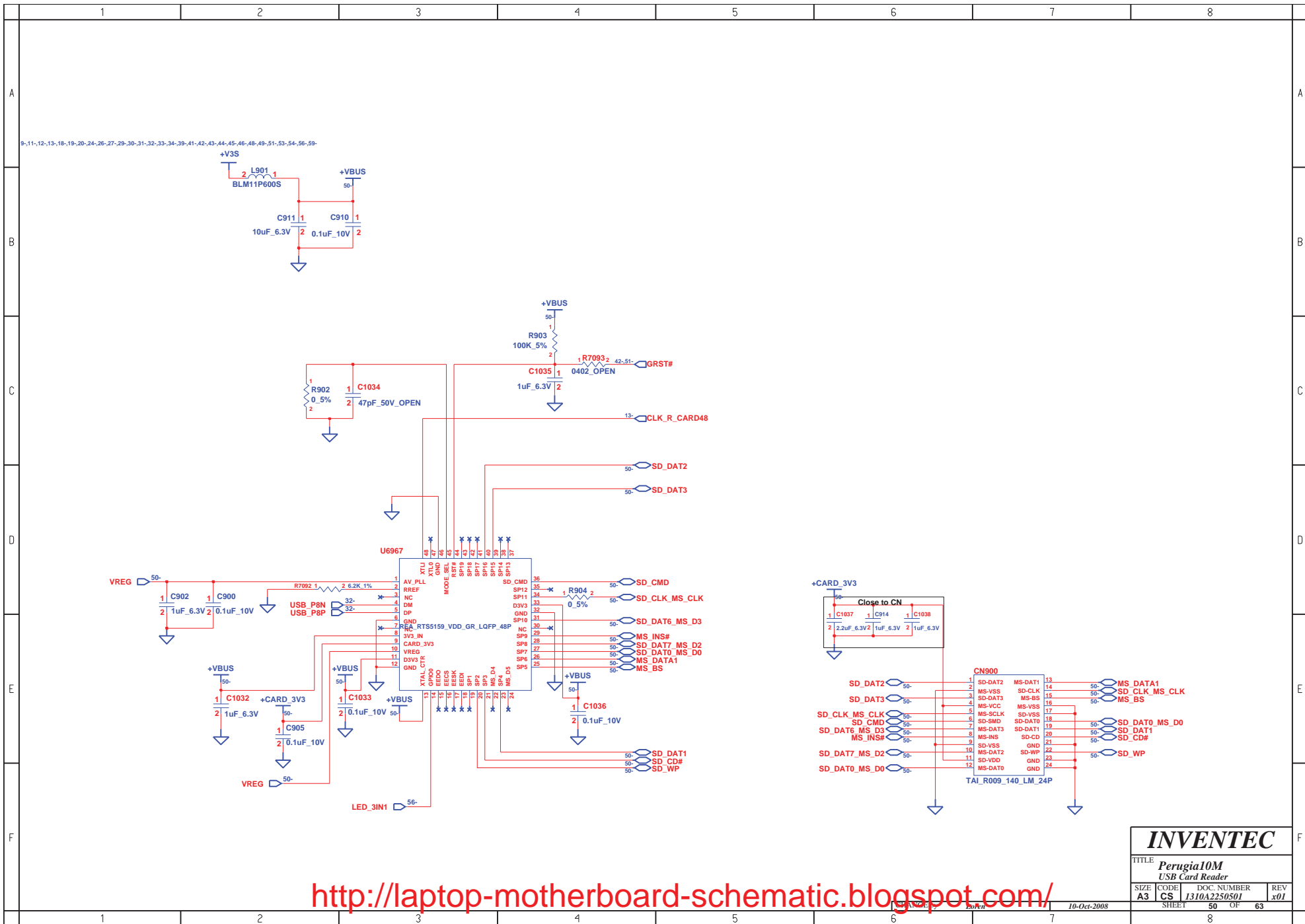
10-Oct-2008



<http://laptop-motherboard-schematic.blogspot.com/>

INVENTEC			
TITLE Perugia10M HDMI CONN			
SIZE A3	CODE CS	DOC. NUMBER I310A2250501	REV x01
SHEET 49		OF 63	

10-Oct-2008

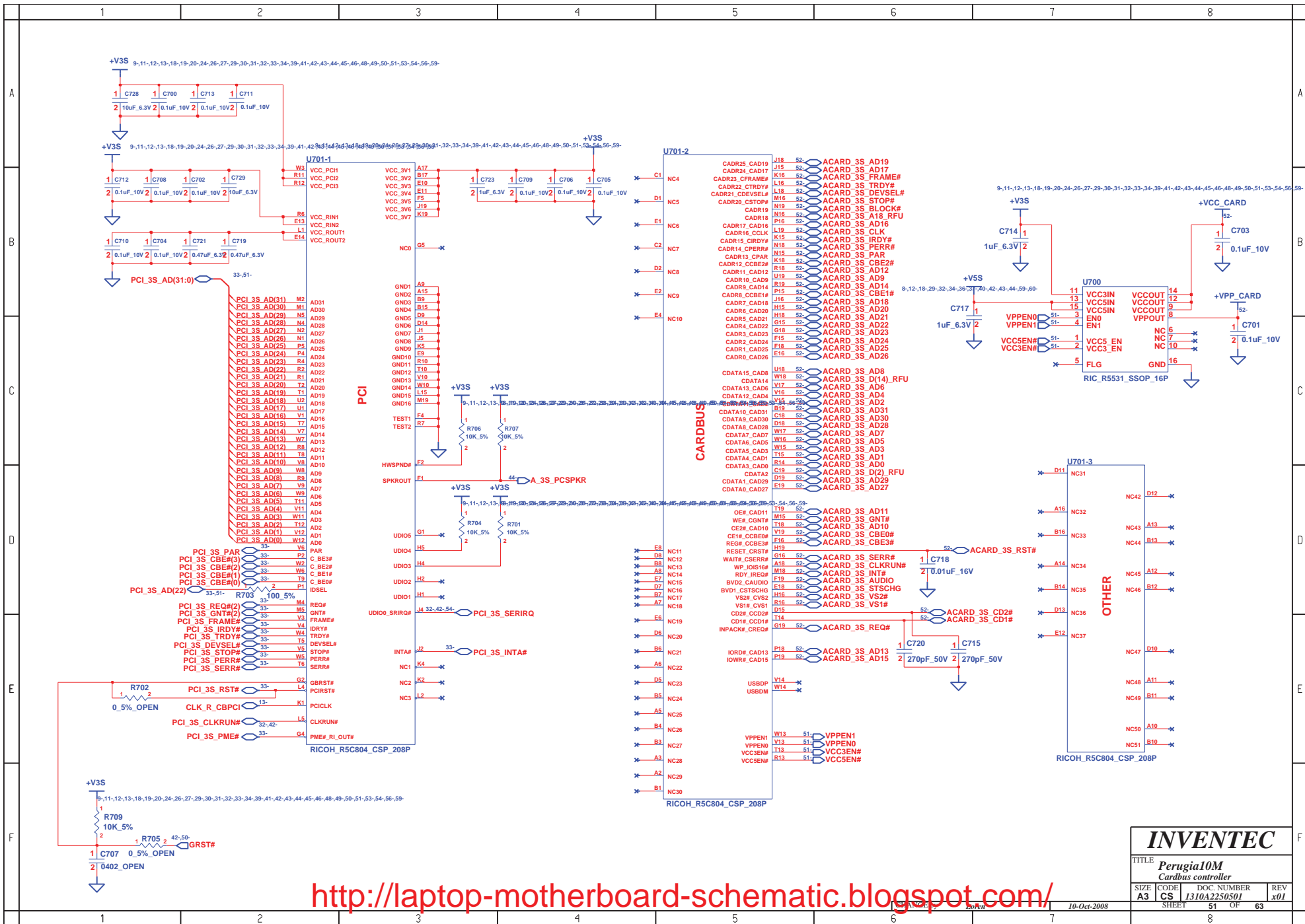


<http://laptop-motherboard-schematic.blogspot.com/>

INVENTEC

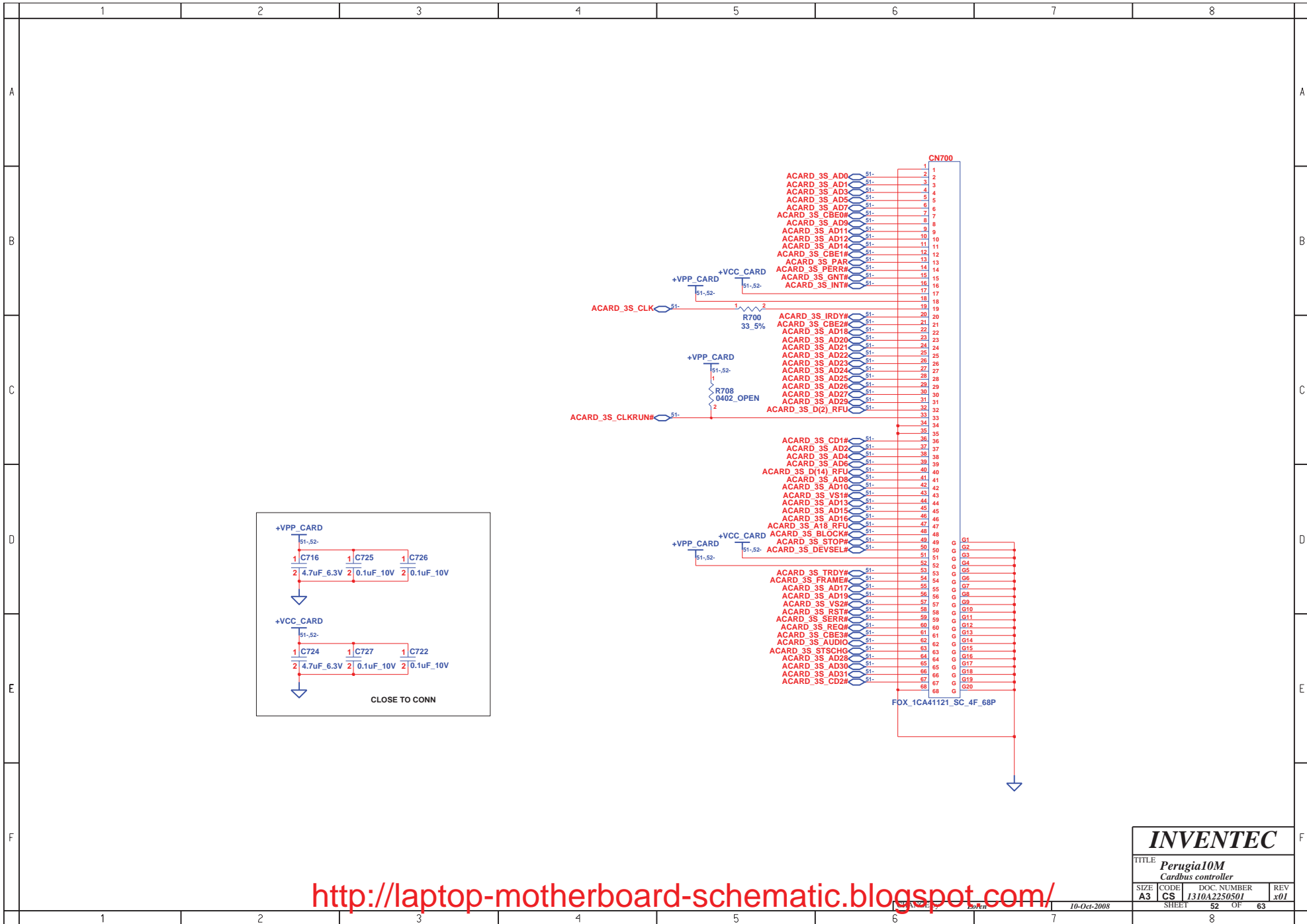
TITLE			
Perugia10M USB Card Reader			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	1310A2250501	x01
SHEET		50	OF 63

10-Oct-2008



<http://laptop-motherboard-schematic.blogspot.com/>

INVENTEC			
TITLE <i>Perugia10M</i> Cardbus controller			
SIZE A3	CODE CS	DOC. NUMBER 1310A2250501	REV x01
SHEET 51		OF 63	



<http://laptop-motherboard-schematic.blogspot.com/>

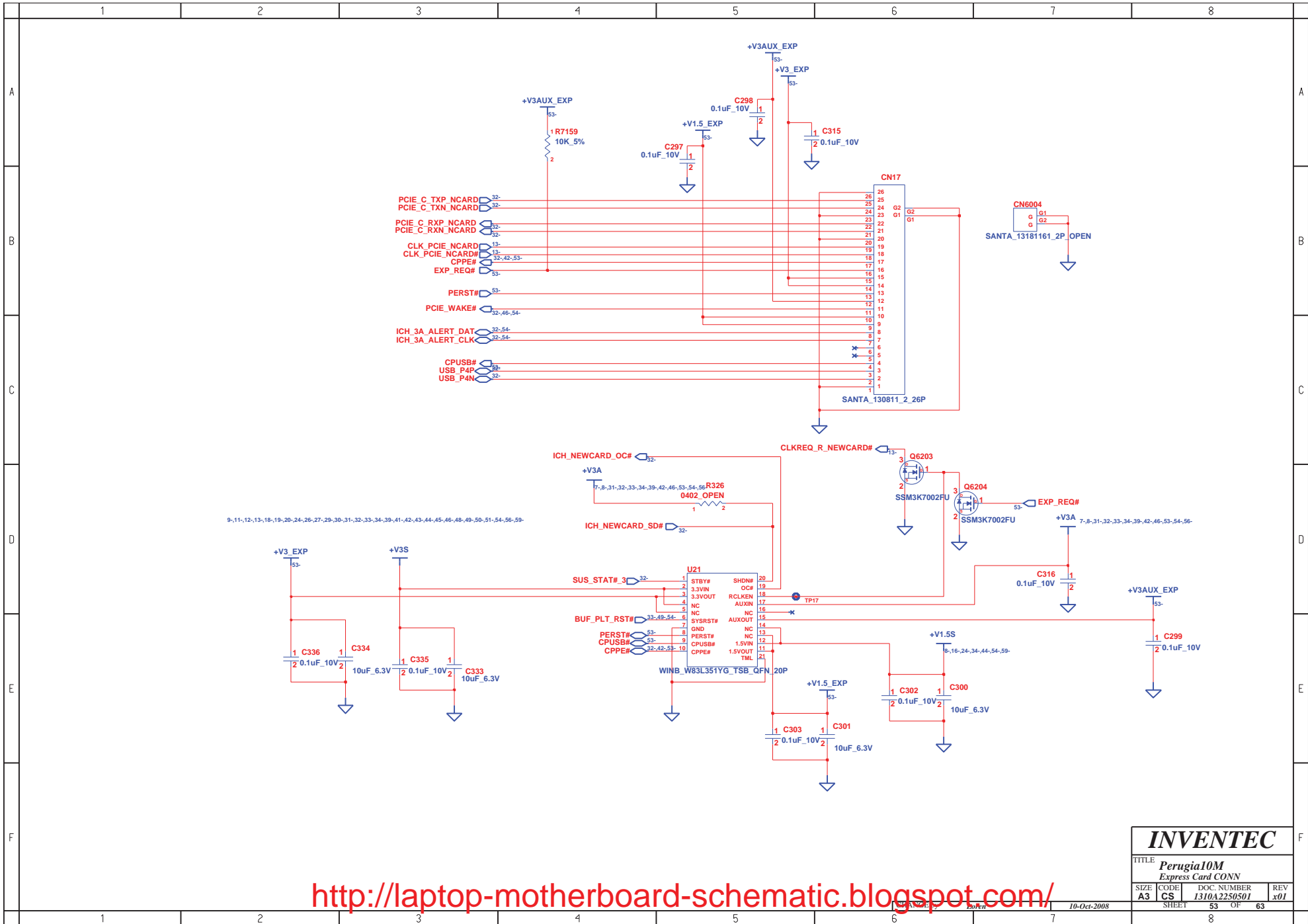
INVENTEC

TITLE
Perugia10M
 Cardbus controller

SIZE	CODE	DOC NUMBER	REV
A3	CS	1310A2250501	x01
SHEET		52	OF 63

10-Oct-2008

8



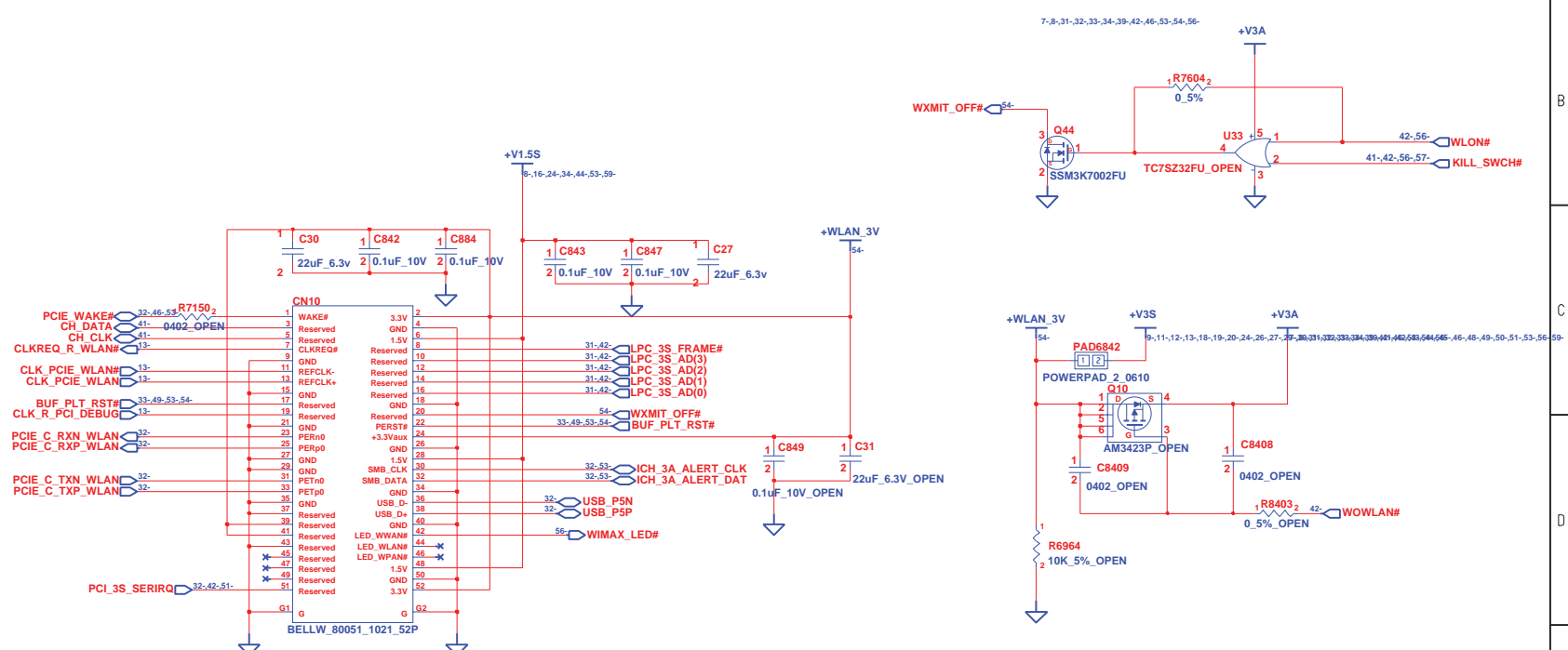
<http://laptop-motherboard-schematic.blogspot.com/>

INVENTEC

TITLE			
Perugia10M			
Express Card CONN			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	1310A2250501	x01
SHEET		53	OF 63

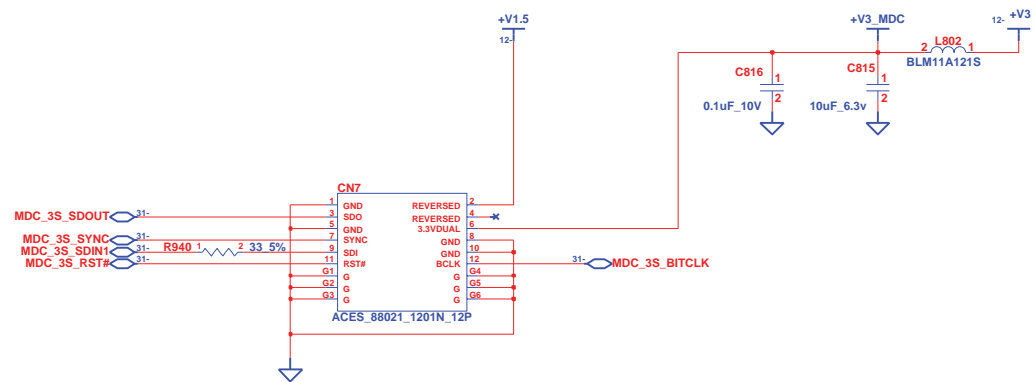
10-Oct-2008

Wireless & Debug card



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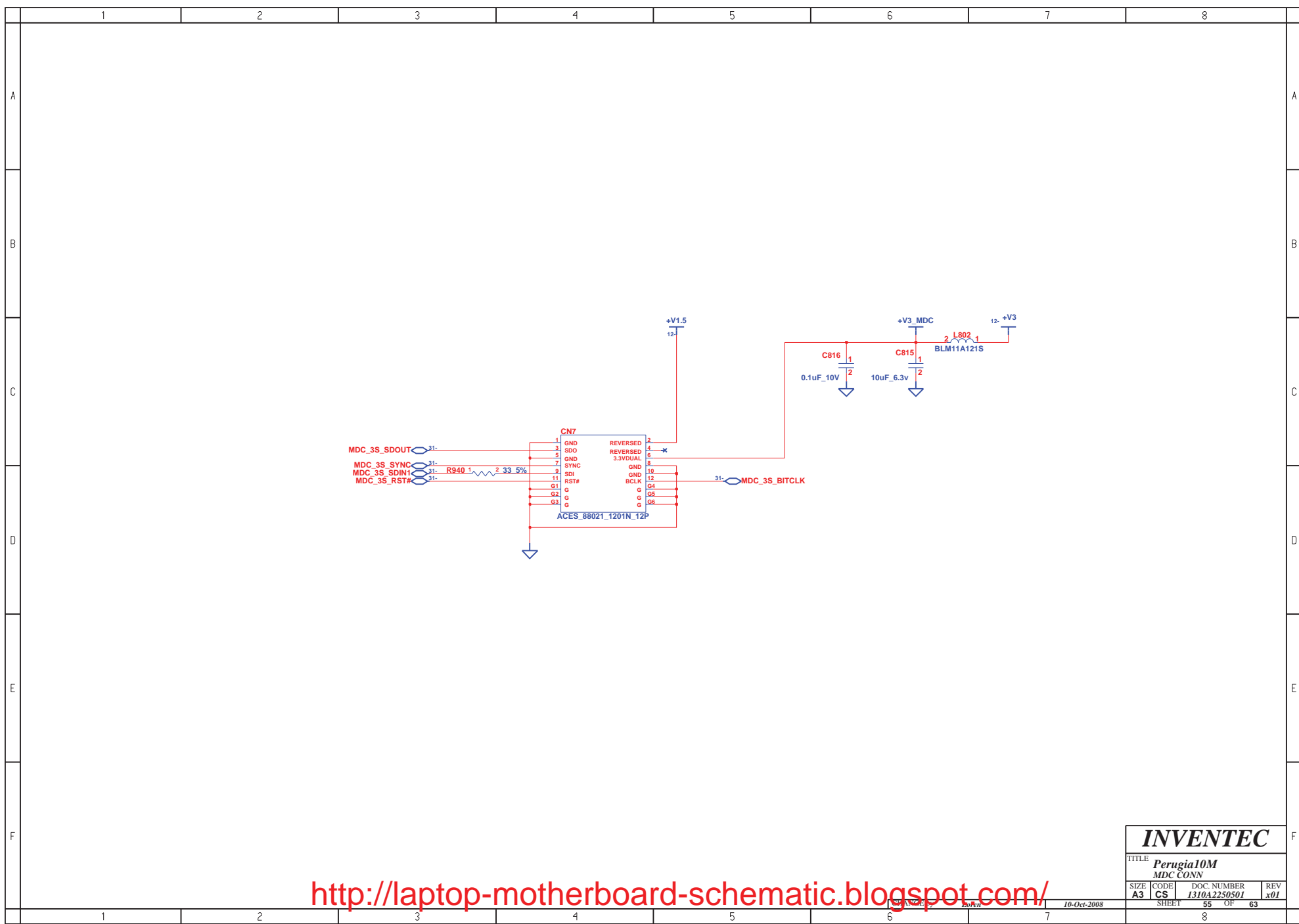
INVENTEC			
TITLE Perugia10M Wireless & debug card			
SIZE A3	CODE CS	DOC. NUMBER 1310A2250501	REV x01
DATE 10-Oct-2008		SHEET 54 OF 63	

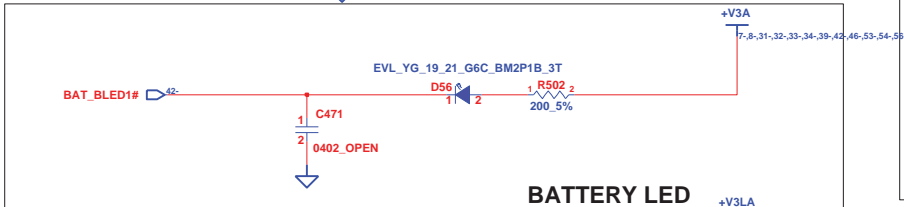
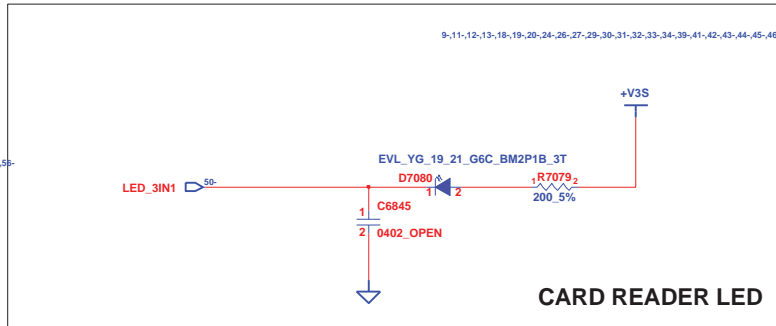
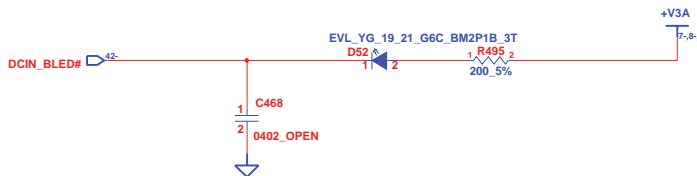
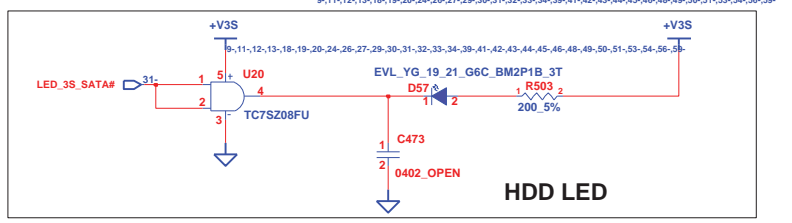
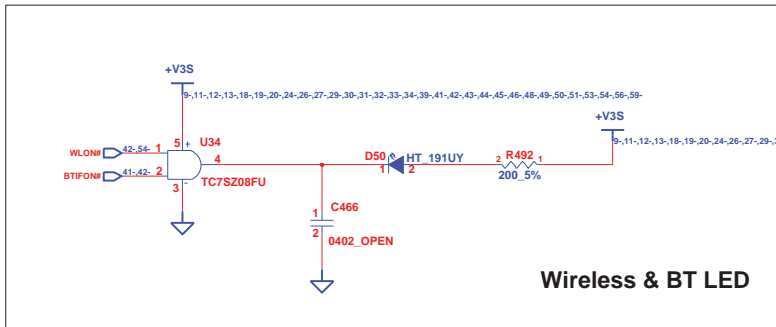
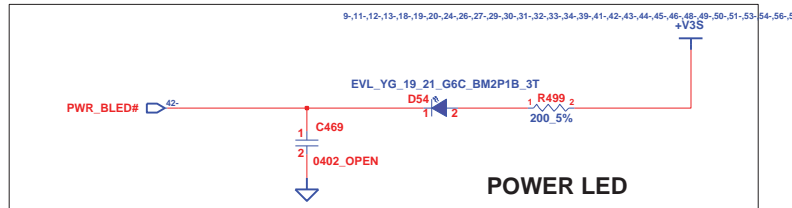
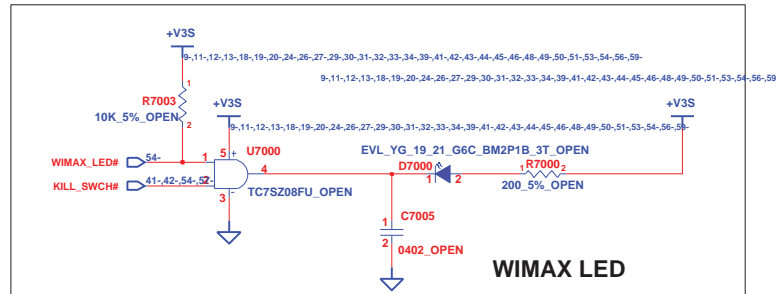
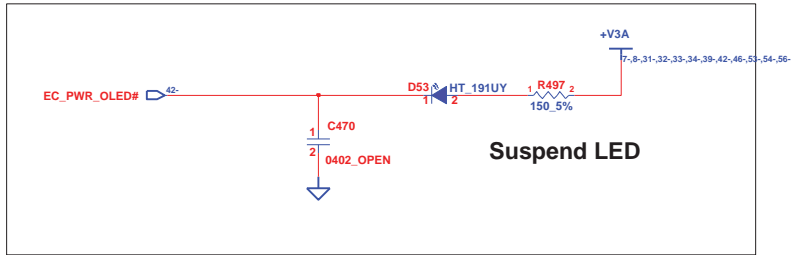


<http://laptop-motherboard-schematic.blogspot.com/>

INVENTEC			
TITLE Perugia10M MDC CONN			
SIZE A3	CODE CS	DOC. NUMBER 1310A2250501	REV x01
SHEET		55	OF 63

10-Oct-2008





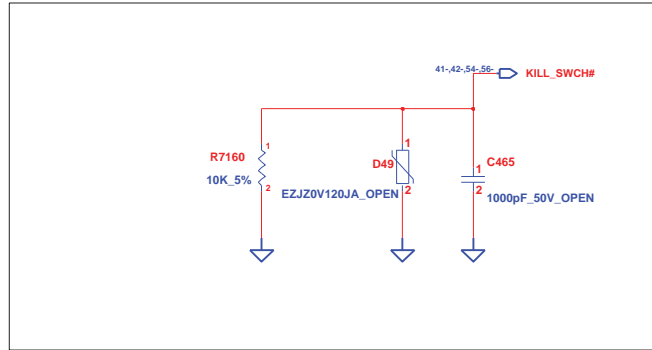
<http://laptop-motherboard-schematic.blogspot.com/>

INVENTEC			
TITLE Perugia10M			
LED & Hotkey CONN			
SIZE A3	CODE CS	DOC. NUMBER 1310A2250501	REV x01
SHEET 56		OF 63	

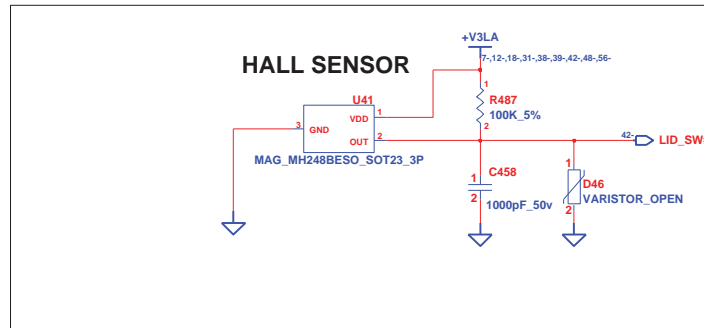
10-Oct-2008

KILL SWITCH

WLAN ON: KILL_SWCH# -> Low
 WLAN OFF: KILL_SWCH# -> High



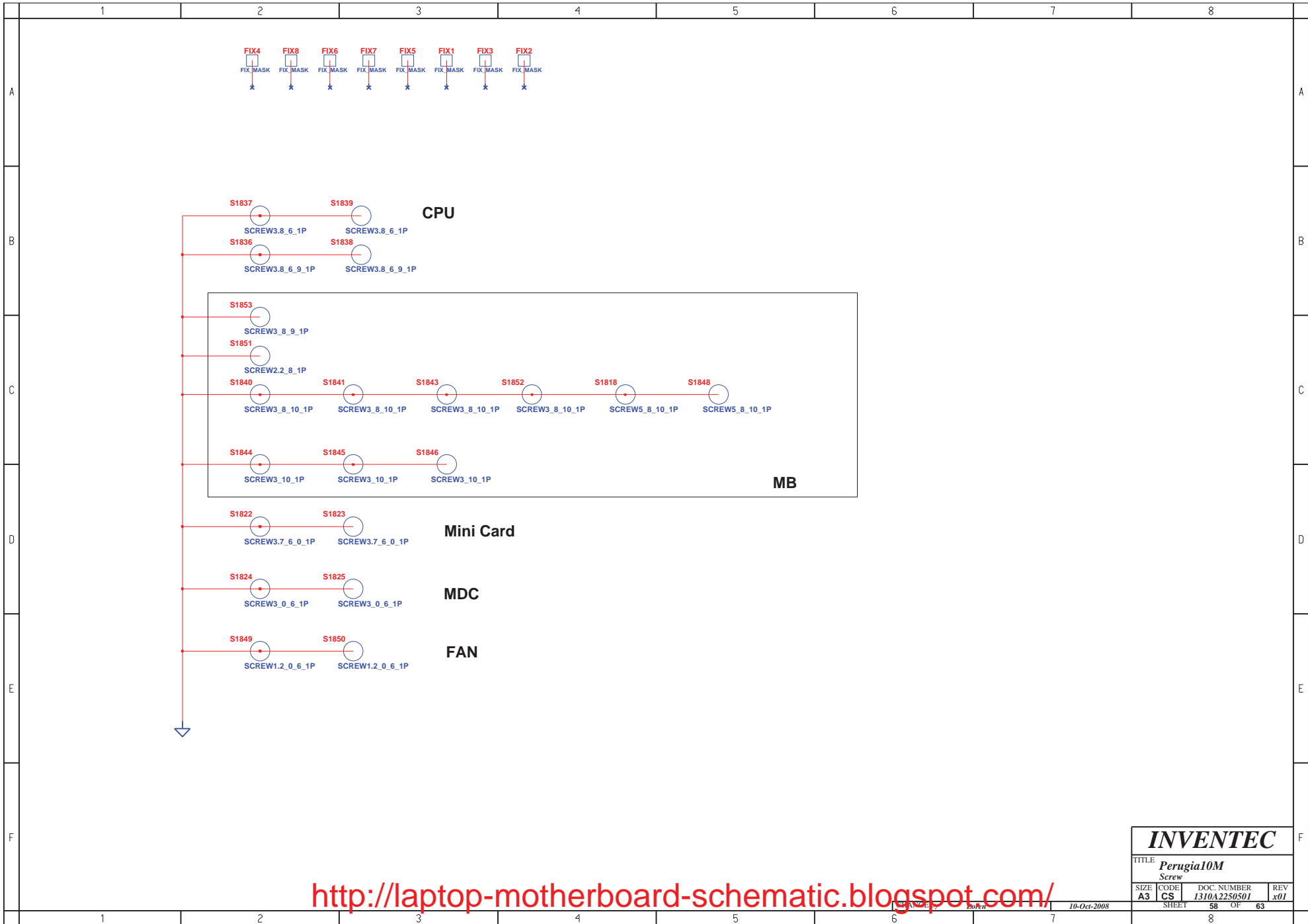
HALL SENSOR



<http://laptop-motherboard-schematic.blogspot.com/>

INVENTEC			
TITLE <i>Perugia10M</i> Kill Swich & Hall Sensor			
SIZE A3	CODE CS	DOC. NUMBER <i>1310A2250501</i>	REV <i>x01</i>
SHEET		57	OF 63

10-Oct-2008

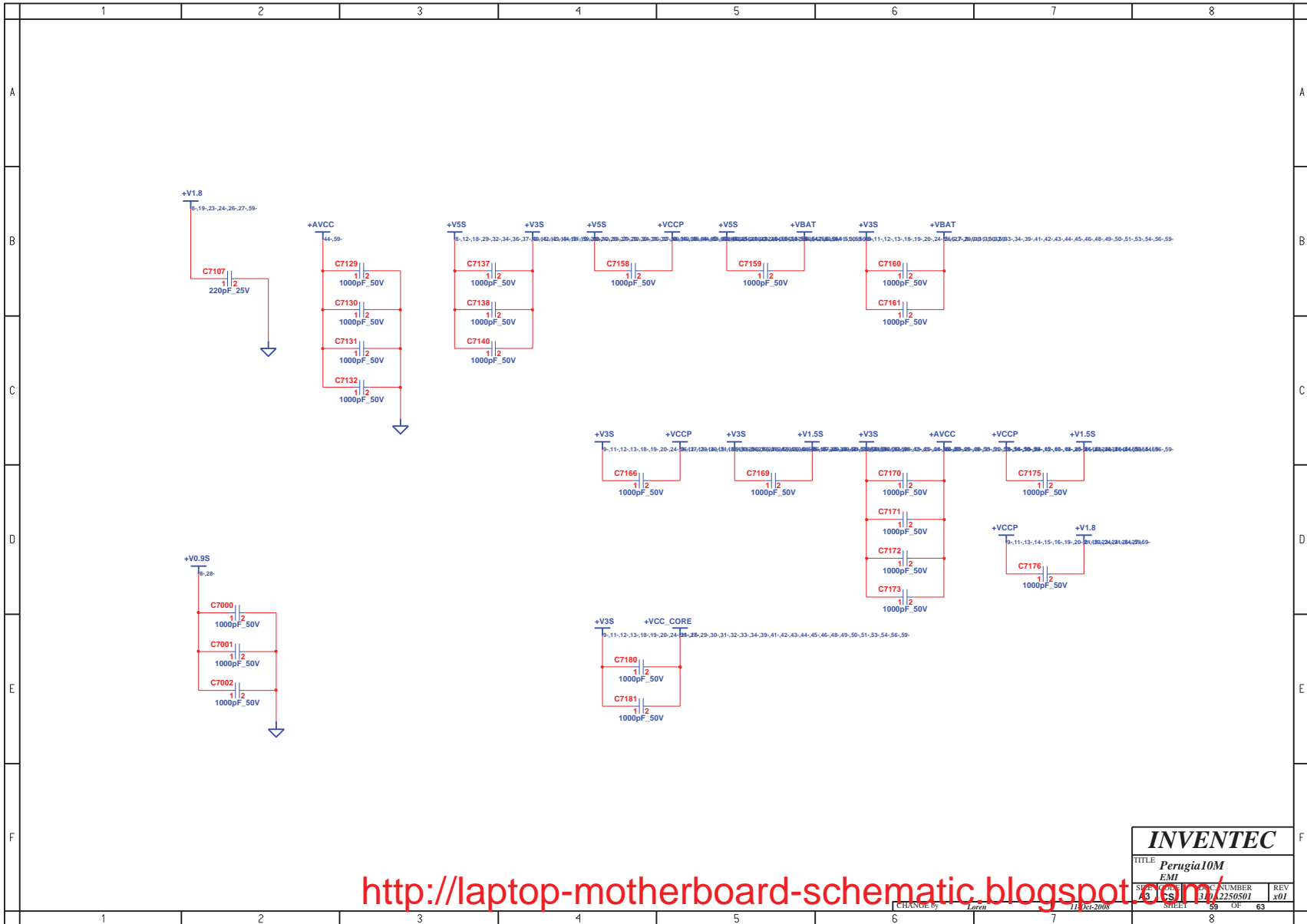


<http://laptop-motherboard-schematic.blogspot.com/>

INVENTEC

TITLE			
Perugia10M			
Screw			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	1310A2250501	x01
SHEET		OF	63
8			

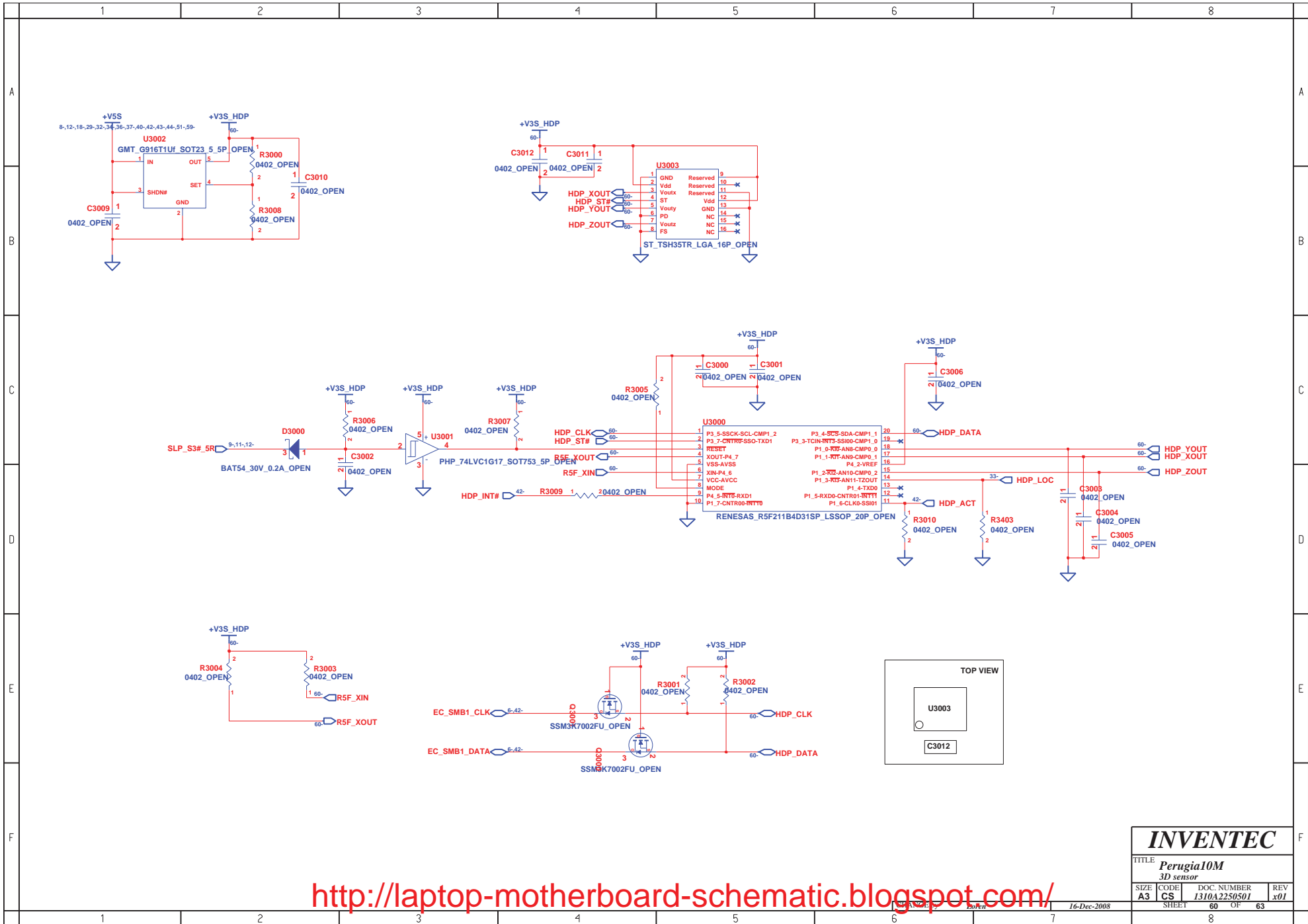
10-Oct-2008



<http://laptop-motherboard-schematic.blogspot.com/>

INVENTEC			
TITLE <i>Perugia10M</i>			
EMI			
SHEET NUMBER	31	2250501	REV x01
DATE	11/02/2008	SHEET 59 OF 63	

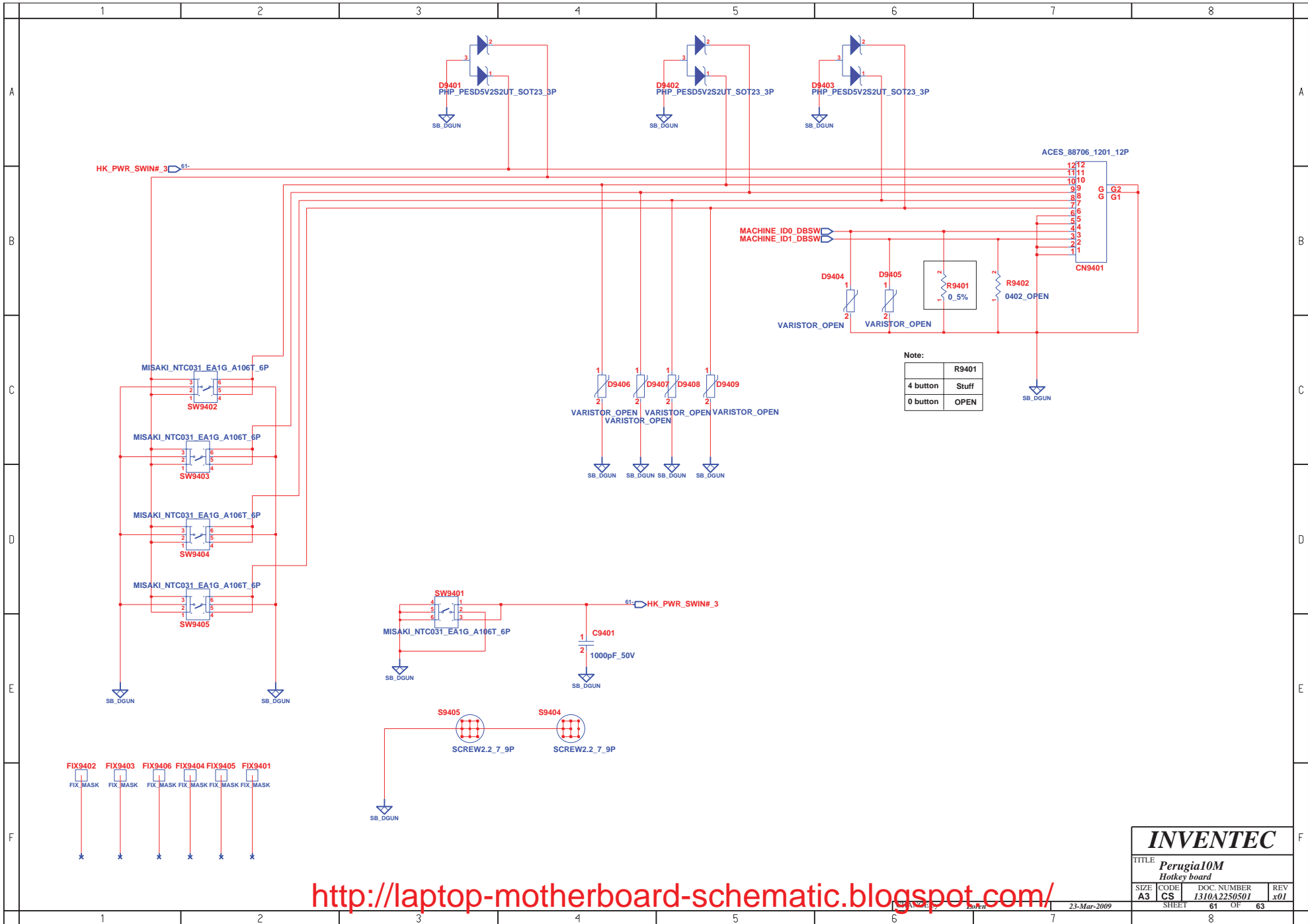
CHANGE BY: Loren 11/02/2008



<http://laptop-motherboard-schematic.blogspot.com/>

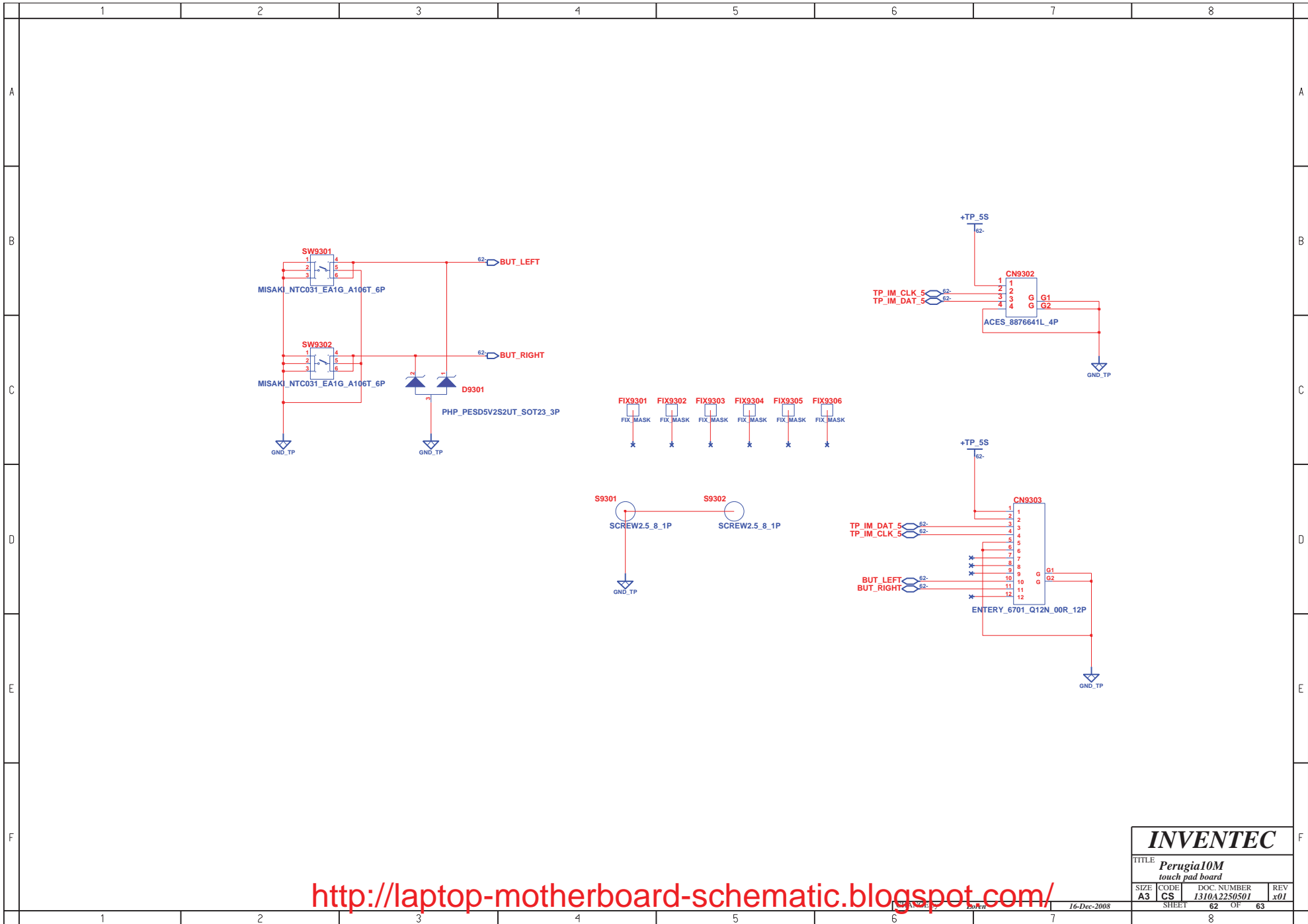
INVENTEC			
TITLE Perugia10M 3D sensor			
SIZE A3	CODE CS	DOC. NUMBER 1310A2250501	REV x01
SHEET 60		OF 63	

16-Dec-2008



<http://laptop-motherboard-schematic.blogspot.com/>

INVENTEC			
TITLE Perugia10M Hotkey board			
SIZE A3	CODE CS	DOC. NUMBER 1310A2250501	REV x01
SHEET 61 OF 63		23-Mar-2009	



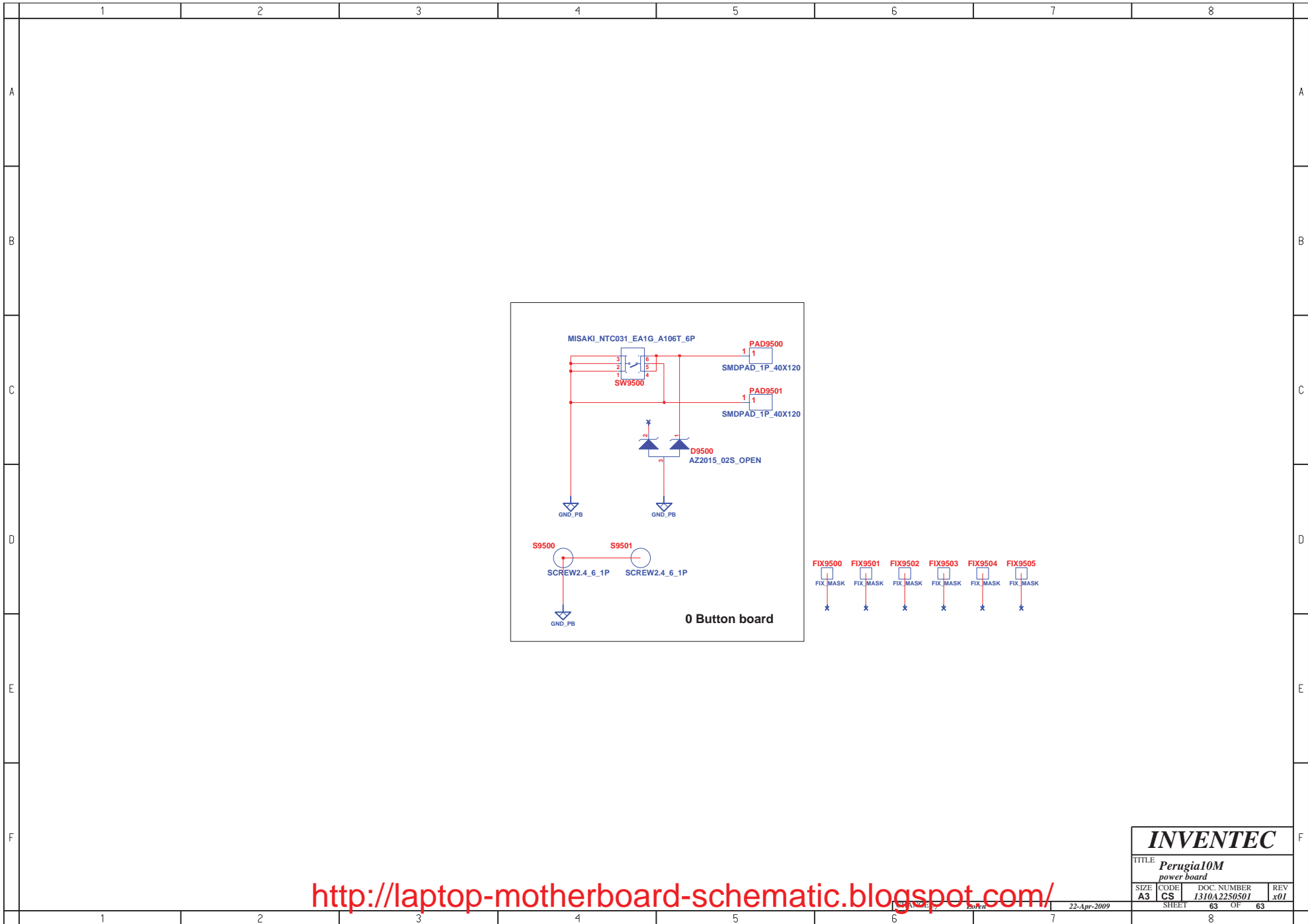
<http://laptop-motherboard-schematic.blogspot.com/>

INVENTEC

TITLE
Perugia10M
touch pad board

SIZE	CODE	DOC. NUMBER	REV
A3	CS	1310A2250501	x01

16-Dec-2008 SHEET 62 OF 63



<http://laptop-motherboard-schematic.blogspot.com/>

INVENTEC			
TITLE <i>Perugia10M</i> power board			
SIZE A3	CODE CS	DOC. NUMBER 1310A2250501	REV x01
SHEET		63	OF 63

22-Apr-2009

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